

SPDT 50 W/125 W_{peak} Power Switch 1 MHz to 5.0 GHz

Features

- Frequency Range 1 MHz to 5.0 GHz
- Low insertion loss:
0.25 dB @ 1.0 GHz
0.35 dB @ 3.0 GHz
0.50 dB @ 5.0 GHz
- High isolation:
39 dB @ 1.0 GHz
27 dB @ 3.0 GHz
18 dB @ 5.0 GHz
- 50 W CW Power, 125 W_{peak} Peak Power
- Low power consumption, less than 1 mW
- No external DC blocking capacitors on RF lines
- All RF ports OFF state
- Versatile 2.6 V – 5.25 V power supply
- No need to supply negative voltages

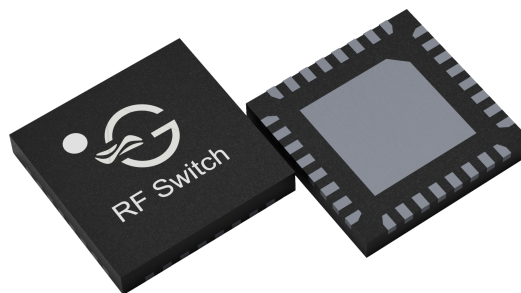


Figure 1: TS8021N in 5.0 x 5.0 mm² QFN 32-pin package.

Applications

- Private mobile and defense radios
- Public safety handsets
- Cellular infrastructure
- Satellite terminals
- Datalinks

General Description

The TS8021N is a 2nd Generation symmetrical reflective Single Pole Double Throw (SPDT) switch designed for high-power switching applications. The TS8021N covers 1 MHz to 5.0 GHz bandwidth and provides low insertion loss, high isolation, and high linearity within a small package size. With 50 W CW power handling and 125 W peak power capability, the TS8021N is well suited for demanding applications requiring superior RF performance. No negative supply voltage or external RF DC-blocking capacitors are required.

The TS8021N is housed in a compact 5.0×5.0 mm² 32-lead Quad Flat No-lead (QFN) plastic package.

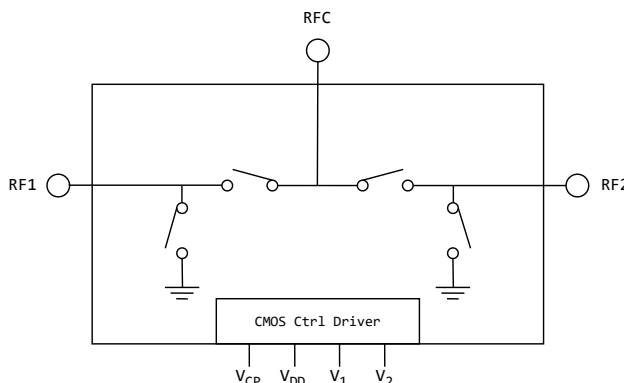


Figure 2: TS8021N functional diagram



RoHS10/REACH/Halogen free

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Ordering information

Table 1: Ordering Information

Device Part Number	Package Type	Notes
TS8021N	32-Pin 5.0x5.0x0.75 mm ³ QFN	Core part number
TS8021N-EVB	Evaluation Board	
TS8021NMTRPBF ¹	330 mm reel, 3 000 pcs	Full reel

¹ MTRPBF - M: Manufacturing, TR: Tape and Reel, and PBF: lead free.

Table 2: Tape and Reel Information

Form	Quantity	Reel Diameter	Reel Width
Tape and Reel	3 000	13" (330mm)	18mm

Pin Assignment

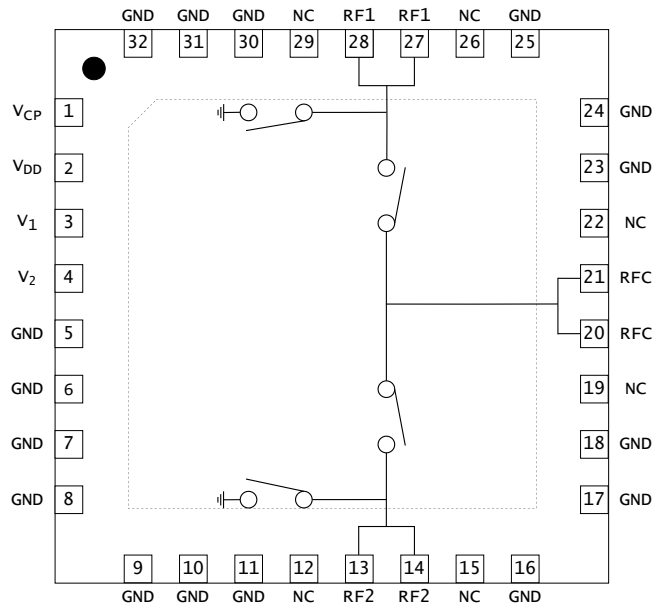


Figure 3: TS8021N pin assignment [top view]

Table 3: Pin Definition

Pin Number	Pin Name	Description
1	V _{CP}	Internal charge pump voltage output, connect a 1nF capacitor to GND on this node.
2	V _{DD}	DC Power Supply
3	V ₁	Switch control input 1
4	V ₂	Switch control input 2
13, 14	RF2	RF port 2, please connect together
20, 21	RFC	RF Common port / Antenna port, please connect together
27, 28	RF1	RF port 1, please connect together
12,15,19,22,26,29	NC	NC, Do not connect for adequate clearance and creepage
5,6,7,8,9,10,11	GND	Connect to ground ²
16,17,18		
23,24,25,30,31,32		
33 ¹	GND	Ground thermal pad, please connect to GND

¹ The backside ground (thermal) pad of the package must be grounded directly to the ground plane of PCB with multiple vias, and adequate heat sinking must be used to ensure proper operation and thermal management.

² These pins are NC pins inside the package. To avoid floating pins around RF lines, we request these to be connected to ground.

Absolute Maximum Ratings

Table 4: Absolute Maximum Ratings $T_A = +25^\circ\text{C}$ unless otherwise specified¹.

Parameter	Symbol	Value	Unit
Electrical Ratings			
Power Supply Voltage	V_{DD}	5.5	V
Storage Temperature Range	T_{st}	-55...+125	$^\circ\text{C}$
Operating Temperature Range	T_{op}	-40...+85	$^\circ\text{C}$
Maximum Junction Temperature	T_j	+140	$^\circ\text{C}$
Max RF CW input power, $\geq 400\text{MHz}$ ³	RFx/RFC	48	dBm
Max RF CW input power, 10-400MHz ³	RFx/RFC	47	dBm
Max RF CW input power, $\geq 400\text{MHz}$, 20% duty cycle, 2.0 ms pulse ^{2,3}	RFx/RFC	50	dBm
Max RF peak input power, $\geq 400\text{MHz}$, 1% duty cycle, 10 μs pulse ²	RFx/RFC	52	dBm
Max RF peak voltage 30MHz	RFx/RFC	100	V
Max RF peak voltage 800MHz	RFx/RFC	100	V
Thermal Ratings			
Thermal Resistance (junction-to-case) – Bottom side	$R_{\theta_{jc}}$	5.0	$^\circ\text{C/W}$
Soldering Temperature	T_{solder}	+260	$^\circ\text{C}$
ESD Ratings			
Human Body Model (HBM)	Level 1B	500...<1000	V
Charged Device Model (CDM)	Level C3	≥ 1000	V
Moisture Rating			
Moisture Sensitivity Level ⁴	MSL	1	

¹ Maximum ratings are absolute ratings. Exposure to absolute maximum rating conditions for extended periods may affect device reliability and can cause permanent damage to the device. Exceeding one or a combination of the absolute maximum ratings may cause permanent and irreversible damage to the device and/or to surrounding circuit. Functional operation of the device is not implied in any conditions above those indicated in the Electrical Specifications section.

² Test frequency 800MHz.

³ See Power De-rating table for low frequencies.

⁴ Tagore recommends to store part in moisture barrier bags to preserve solderability.

Electrical Specifications

Table 5: Electrical Specifications $T_A = +25^{\circ}\text{C}$; $V_{DD} = +3.3\text{V}$; 50Ω Source/Load.

Parameter	Condition	Minimum	Typical	Maximum	Unit
Operating frequency	f	1		5000	MHz
Insertion loss	30 MHz		0.15		dB
unmatched, upto 3.5 GHz	1.0 GHz		0.20	0.30	dB
RFC – RFx	3.0 GHz		0.45		dB
	3.5 GHz		0.70		dB
Insertion loss ¹	30 MHz		0.15		dB
matched upto 5.0 GHz	1.0 GHz		0.25		dB
RFC – RFx	3.0 GHz		0.35		dB
	5.0 GHz		0.50		dB
Isolation	30 MHz		50		dB
unmatched, upto 3.5 GHz	1.0 GHz		43		dB
RFC – RFx	3.0 GHz		27		dB
	3.5 GHz		25		dB
Isolation ¹	30 MHz		50		dB
matched upto 5.0 GHz	1.0 GHz		39		dB
RFC – RFx	3.0 GHz		27		dB
	5.0 GHz		18		dB

¹ Matched values are not guaranteed as they include performance of matching components. These components are beyond control of TagoreTech and therefore given values are indications, not guaranteed values.

Table 6: Electrical Specifications $T_A = +25^\circ\text{C}$; $V_{DD} = +3.3\text{V}$; 50Ω Source/Load.

Parameter	Condition	Minimum	Typical	Maximum	Unit
Operating frequency	f	1		5000	MHz
Return Loss	30 MHz		-30		dB
unmatched, upto 3.5 GHz	1.0 GHz		-22		dB
RFC – RFx	3.0 GHz		-14		dB
	3.5 GHz		-13		dB
Return Loss ¹	30 MHz		-30		dB
matched upto 5.0 GHz	1.0 GHz		-17		dB
RFC – RFx	2.0 GHz		-13		dB
	3.0 GHz		-17		dB
	4.0 GHz		-17		dB
	5.0 GHz		-17		dB

¹ Matched values are not guaranteed as they include performance of matching components. These components are beyond control of TagoreTech and therefore given values are indications, not guaranteed values.

Table 7: Electrical Specifications $T_A = +25^\circ\text{C}$; $V_{DD} = +3.3\text{V}$; 50Ω Source/Load.

Parameter	Condition	Minimum	Typical	Maximum	Unit
Operating frequency	f	1		5000	MHz
Harmonic Distortion					
H_2	800MHz, $P_{in} = 45\text{dBm}$		-86		dBc
H_3	800MHz, $P_{in} = 45\text{dBm}$		-89		dBc
IIP3	800MHz		71		dBm
Power and Compression point					
P_{maxCW}^2	Max RF CW Power 400- f_{max}		47		dBm
P_{maxCW}^2	Max RF CW Power 10-400		46		dBm
P_{maxpeak}	Max RF Peak Power 400- f_{max}		51		dBm
$P_{0.1\text{dB}}$	800MHz, CW		47		dBm
$P_{\text{peak}0.1\text{dB}}$	800MHz, 1% duty cycle, $10\mu\text{s}$ pulse		48		dBm
$P_{1\text{dB}}^1$	800MHz, CW		49		dBm
$P_{\text{maxhot RFX}}^5$	Max RF CW Power, hot switching		40		dBm
$P_{\text{maxhot RFC}}^5$	Max RF CW Power, hot switching		40		dBm

¹ $P_{1\text{dB}}$ has been given for comparison reasons only. Please do not exceed Absolute Maximum ratings.² See Power De-rating table⁵ Dependent on thermal design and surrounding circuits.

Table 8: Electrical Specifications $T_A = +25^\circ\text{C}$; $V_{DD} = +3.3\text{V}$; 50Ω Source/Load.

Parameter	Condition	Minimum	Typical	Maximum	Unit
Operating frequency	f	1		5000	MHz
Noise					
CP switching noise ⁴	RBW=1kHz		-140		dBm
Switching Time					
t_{ON} ⁸	Switch ON time		5.2	9.0	μs
t_{OFF} ⁸	Switch OFF time		5.2	9.0	μs
t_{RISE} ⁸	Switch RISE time		4.5	5.0	μs
t_{FALL} ⁸	Switch FALL time		4.5	7.0	μs
t_{wON}	Minimum Switch ON time		3.6		μs
t_{wOFF}	Minimum Switch OFF time		3.6		μs
$f_{PRR}, C_{VCP} = 1\text{nF}^3$	Maximum pulse repetition rate		1		kHz
$f_{PRR}, C_{VCP} = 10\text{nF}^3$	Maximum pulse repetition rate		1.4		kHz
$f_{PRR}, C_{VCP} = 100\text{nF}^3$	Maximum pulse repetition rate		1.8		kHz
$f_{PRR}, V_{CPext} = -18\text{V}^6$	Maximum pulse repetition rate		>20		kHz
$t_{startup}, C_{VCP} = 1\text{nF}^3$	startup time		0.8		ms
$t_{startup}, C_{VCP} = 10\text{nF}^3$	startup time		7		ms
$t_{startup}, C_{VCP} = 100\text{nF}^3$	startup time		40		ms
Power Supply, DC					
Control voltage ⁷	Power Supply V_{DD}	2.6	3.3	5.25	V
	All control pins high, V_{ih}	1.0	3.3	5.25	V
	All control pins low, V_{il}	-0.3	0	0.5	V
Control current	All control pins high, I_{ih}			7.5	μA
	All control pins low, I_{il}		0		μA
Current consumption	I_{DD} , active mode (V_{DD} on)		160	260	μA

³ With internal charge pump and with C_{VCP} .⁴ Above 250 MHz. For operation at VHF frequencies, below 250 MHz, consider charge pump bypass switch version, if charge pump noise is critical for your application. Please contact TagoreTech for more information.⁵ Dependent on thermal design and surrounding circuits.⁶ External -18 V applied to V_{CP} pin.⁷ Control voltage V_{ih} cannot be higher than V_{DD} (to avoid forward biasing of ESD diode)⁸ Switching time characterization done at 30 Hz and 500 Hz pulse repetition rate (rate to change switch state, for example between RFC-RF1 and RFC-RF2).

Switching time definition

Example of the definition by using 10 W/40 dBm signal. We apply 10W signal to RF port, stabilized with isolator. Isolator is needed as our switch shows to RF port approximately 4Ω impedance. Let's assume that the switch insertion loss is 0.3 dB. Therefore 90% of the RF signal is 39.55 dBm and 10% of the RF signal is 1W/30dBm. We need to take into account 0.3dB insertion loss, therefore numbers are 39.25 dBm and 29.7 dBm respectively. We change the control from low to high and our time reference point is when our control signal exceeds lower threshold value V_{ihlow} . In certain measurements when control signal rise time is significantly shorter than RF output signal, we approximate start of the clock with 50% point of the control signal.

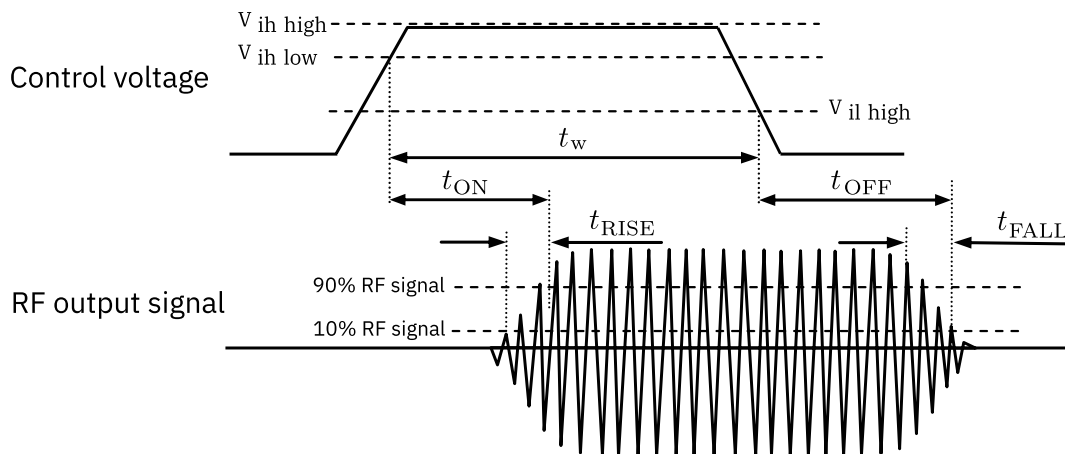


Figure 4: Switching time definition, t_{ON} , t_{OFF} , t_{RISE} , t_{FALL} . Minimum pulse width t_w .

Our component uses integrated charge pump. Maximum pulse repetition rate defines what is maximum frequency for switching events. Please do not exceed given maximum frequency. By feeding external -18 V to V_{CP} , one can improve maximum pulse repetition rate f_{PRR} . Feeding external -18V to V_{CP} , one can drive switch at least to 20kHz f_{PRR} . Expect current consumption of 10mA of -18V.

Switch Control table

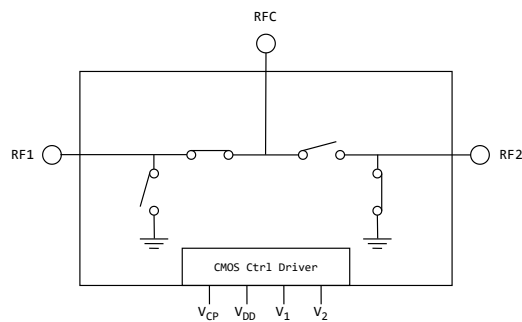
Table 9: Switch Control Table

V_{DD}^1	V_2	V_1	Switch state
1	0	0	RFC – RF1 ON ²
1	0	1	RFC – RF2 ON
1	1	0	ISO (Isolation), RFC port open, RF ports shorted ³ .
0	0	0	V_{DD} off (Isolation), all FETs are on, short shown to RFC and RF ports

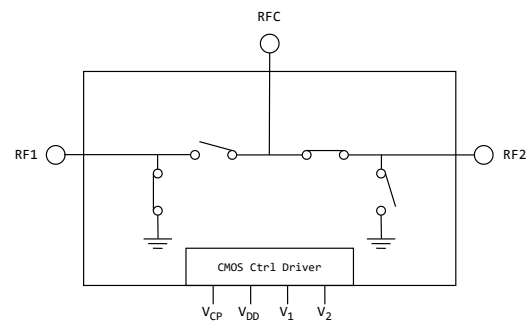
¹ V_{DD} should be applied first before V_1 and V_2 , otherwise may cause damage to the device.

² There are internal pull-downs to ground on both V_1 and V_2 control pins, the state at start-up without any control voltage applied will be RFC – RF1 ON.

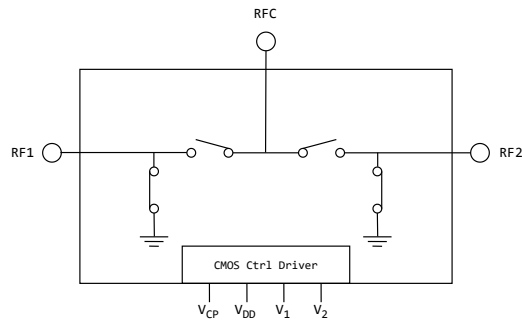
³ If ISO state is not used, the switch can be operated with single control pin V_1



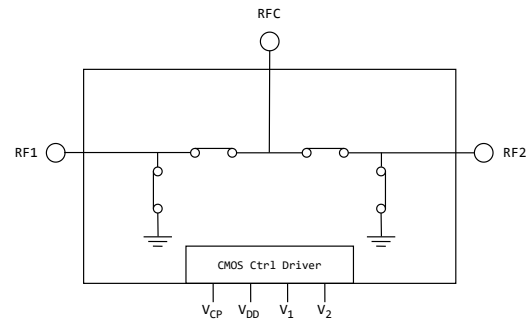
RFC-RF1 ON



RFC-RF2 ON



ISO (Isolation state)

 V_{DD} off state

Theory of Operation

Isolation modes

TS8021N has two isolation modes. Both Isolation mode measurements are shown at electrical performance section. Both modes described below.

Device powered off

When V_{DD} is 0V, all switch RF transistors are on, ie. every MOSFET is on. This means that series MOSFETs and shunt MOSFETs are conducting. Every RF port is essentially connected to ground, including RFC port. This is practical when device is directly connected to RFC, there is certain protection against induced electrical fields. This can partially protect radio equipment against electrical fields, when device is not in usage.

Device powered, ISO state

In this state, series MOSFETs are OFF, ie. OPEN and shunt MOSFETs are on, ie. closed. RF1 to RF2 isolation is maximum and antenna is completely isolated. RFC port is open, whereas RFx ports are showing effectively short. Low frequency isolation in this case is approximately 60dB as basically there is DC block due to open FETs.

Feeding external V_{CP} voltage of -18 V

TS8021N supports external voltage supply to V_{CP} to increase pulse repetition rate f_{PRR} . With external voltage supply, f_{PRR} can be increased at least to support pulse repetition rate more than 20 kHz, in some cases even up to 100 kHz. In order to use external voltage, it is mandatory to supply first V_{DD} , have a delay of 1 ms and then apply -18 V to V_{CP} . Shutdown should be in opposite order.

Applications

The TS8021N offers 50 W CW with 125 W_{peak} capability across the 1 MHz to 5.0 GHz frequency band. Target applications include narrowband and multi-octave wideband radios, jamming systems, EMC testing equipment, public mobile radios, and industrial/scientific instrumentation. Traditionally, such applications relied on power-hungry and complex PIN diode solutions; the TS8021N significantly reduces design complexity for these RF switching requirements. The device operates without external matching components for frequencies up to 2.5 GHz; for operation above 2.5 GHz, implementing matching networks is recommended. This datasheet provides an example matching configuration and corresponding performance characterization.

Schematics and Evaluation Board

S-parameters of the both presented EVBs can be downloaded from link: [Download TS8021N S-parameters](#)

Table 10: Port definitions in s-parameter files and plots.

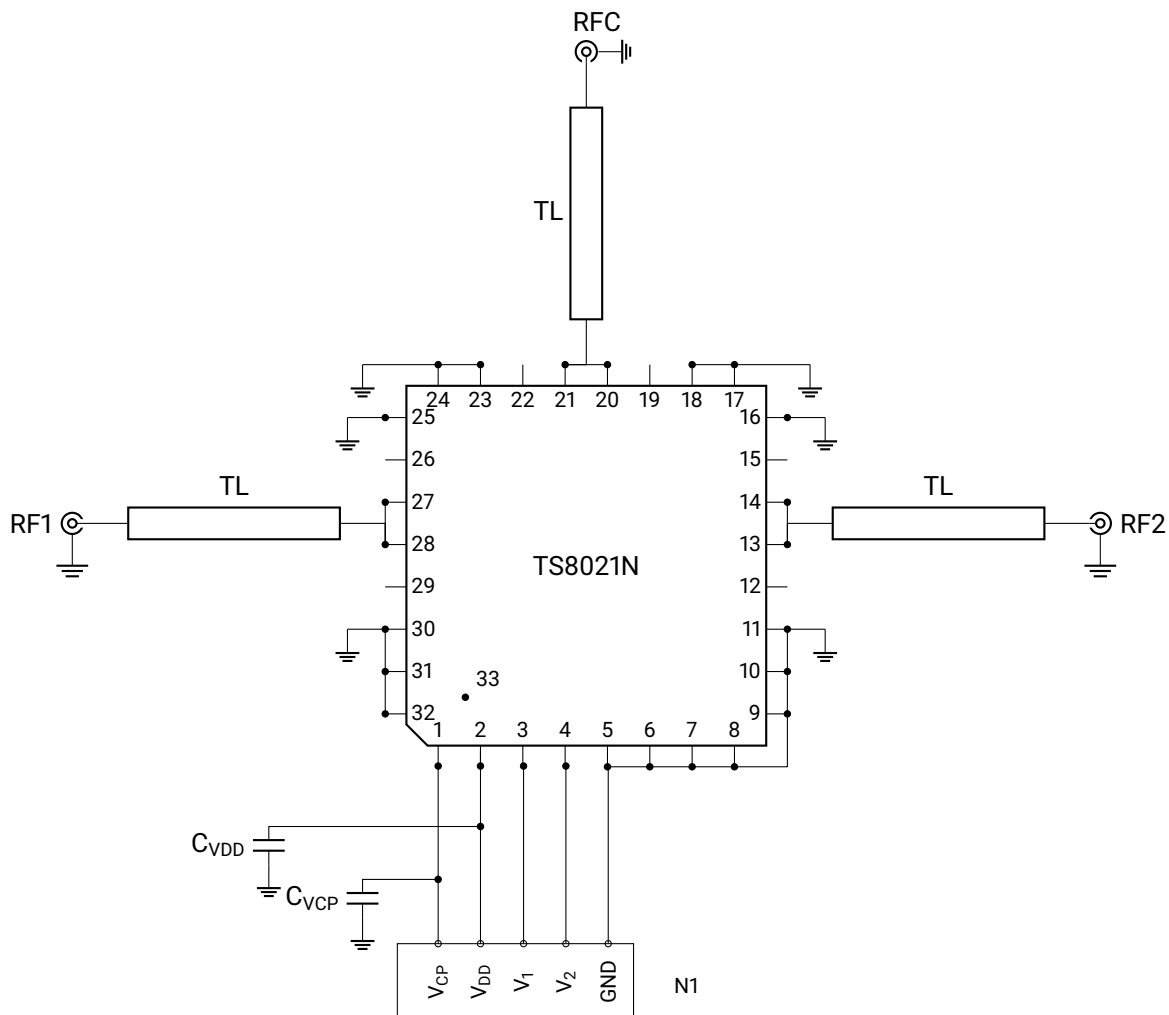
PIN name	Port numbers	S-parameters	Function
RFC	1	S11	
RF1	2	S22	
RF2	3	S33	
RF1ON	12	S21	RFC-RF1 ON
RF2ON	13	S31	RFC-RF2 ON

Content of s-parameter repository:

```
TS8021N.zip
├── IDX_00_no_match.....Directory for s-parameters without matching components
│   ├── Readme.txt
│   ├── TS8021N_RF1ON_3PORT.s3p.....S3P file for RFC-RF1ON mode
│   ├── TS8021N_RF2ON_3PORT.s3p.....S3P file for RFC-RF2ON mode
│   ├── TS8021N_IDX00_IL_RL.png.....insertion loss and return loss plot
│   └── TS8021N_IDX00_ISO.png.....isolation plot
├── IDX_01_wideband.....Directory for s-parameters with matching components, 5.0 GHz
│   ├── Readme.txt
│   ├── TS8021N_RFOFF_3PORT.s3p.....S3P file for ISO mode
│   ├── TS8021N_VDDOFF_3PORT.s3p.....S3P file for VDD off mode
│   ├── TS8021N_RF1ON_3PORT.s3p.....S3P file for RFC-RF1ON mode
│   ├── TS8021N_RF2ON_3PORT.s3p.....S3P file for RFC-RF2ON mode
│   ├── TS8021N_IDX01_IL_RL.png.....insertion loss and return loss plot
│   └── TS8021N_IDX01_ISO.png.....isolation plot
```

Performance upto 2.5 GHz, unmatched

The TS8021N delivers optimal performance up to 2.5 GHz without requiring matching components. Only two external components are recommended: a 1 nF capacitor for the V_{CP} line and a 10 nF capacitor for the V_{DD} line to support the charge pump and power supply decoupling.



Schematics of TS8021N EVB

Table 11: Components used for TS8021N EVB

Reference	Part number / Value	Description	Notes
C_{VDD}	10nF	Capacitor	GRM155R71H103KA88
C_{VCP}	0.1 μ F	Capacitor	GRM155R61E104KA87D

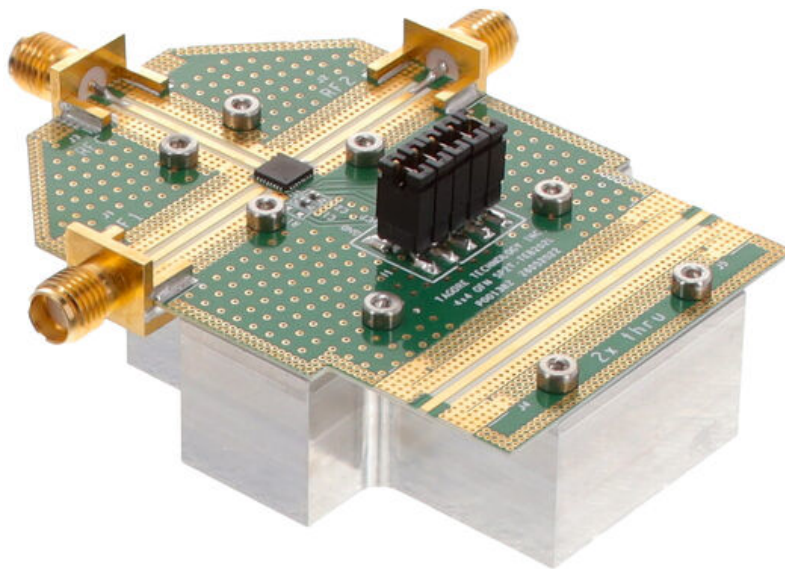
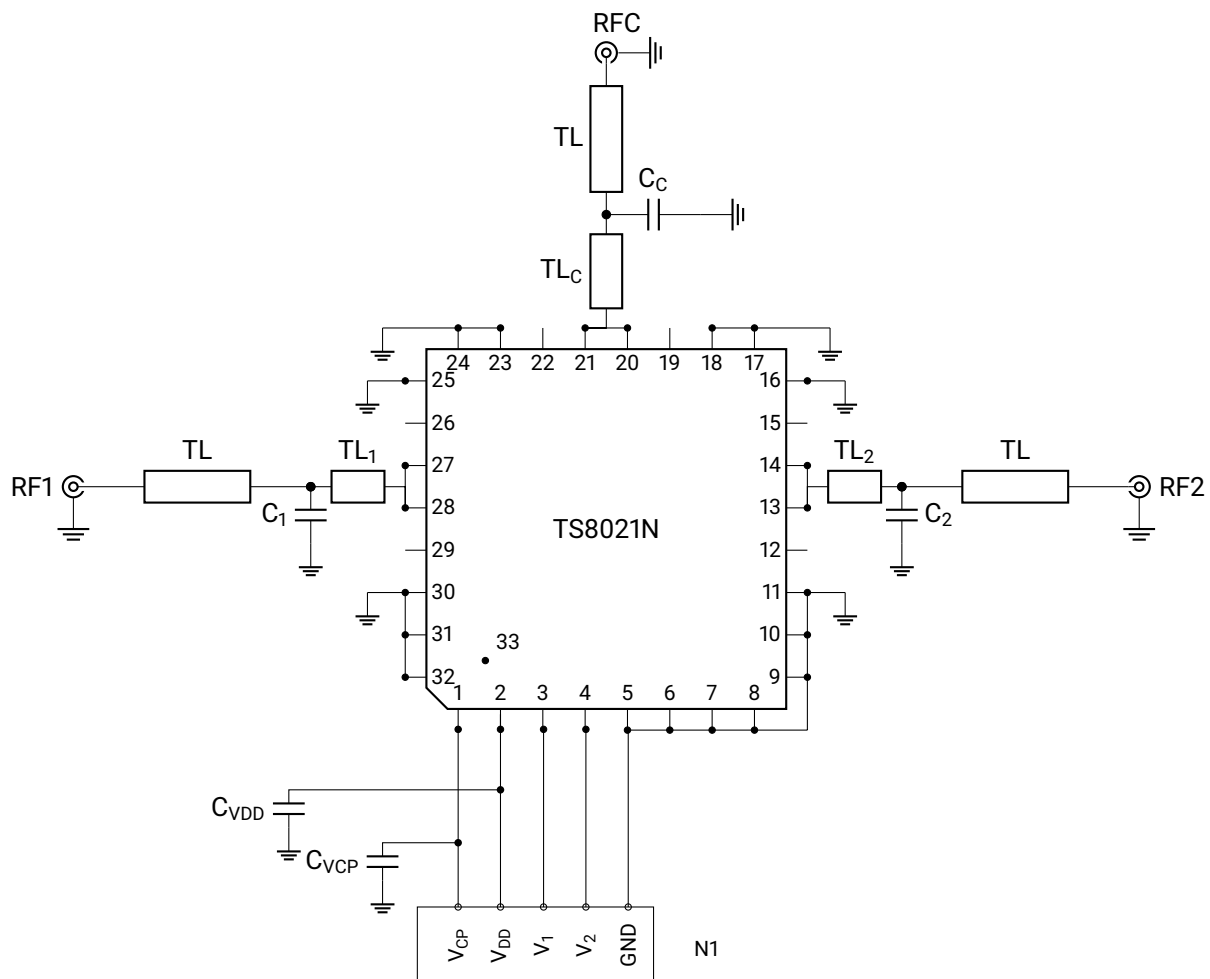


Photo of TS8021N EVB

Performance upto 5.0 GHz, matched



Schematics of TS8021N EVB with matching components.

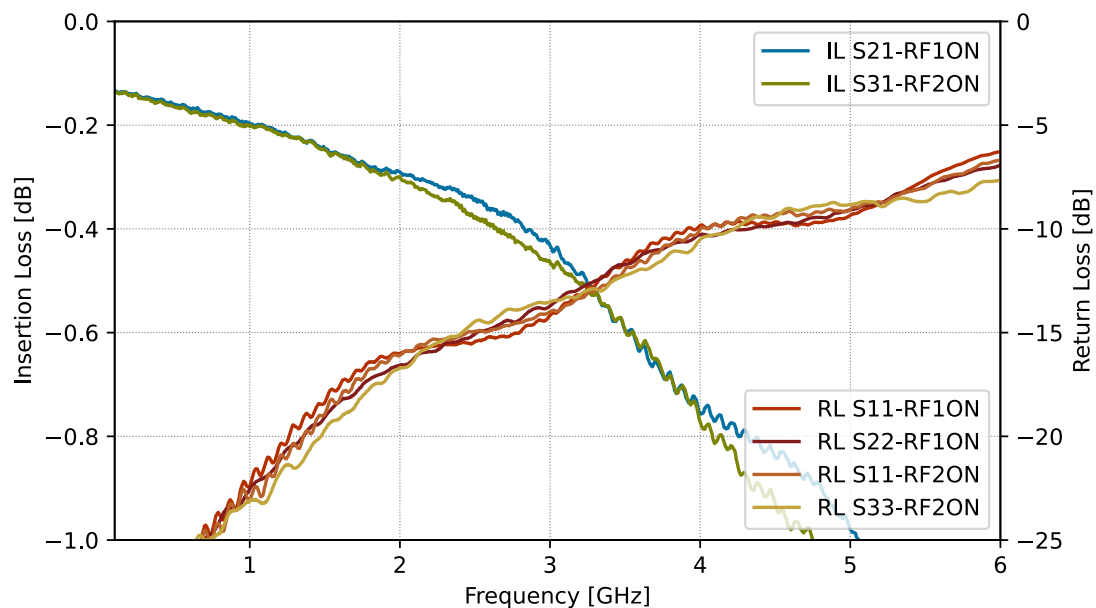
Table 12: Matching components used for 5.0 GHz performance

Reference	Part number / Value	Description	Notes
TL _C	5.1mm	CPW length	Edge of SW to edge of capacitor
C _C	0603N0R3BW251	Ceramic capacitor	0.3pF, 250V, ± 0.1pF
TL ₁	7.9mm	CPW length	Edge of SW to edge of capacitor
C ₁	0603N0R1BW251	Ceramic capacitor	0.1pF, 250V, ± 0.1pF
TL ₂	7.9mm	CPW length	Edge of SW to edge of capacitor
C ₂	0603N0R1BW251	Ceramic capacitor	0.1pF, 250V, ± 0.1pF
C _{VDD}	GRM155R71H103KA88	Capacitor	10nF
C _{VCP}	GRM155R61E104KA87D	Capacitor	0.1μF

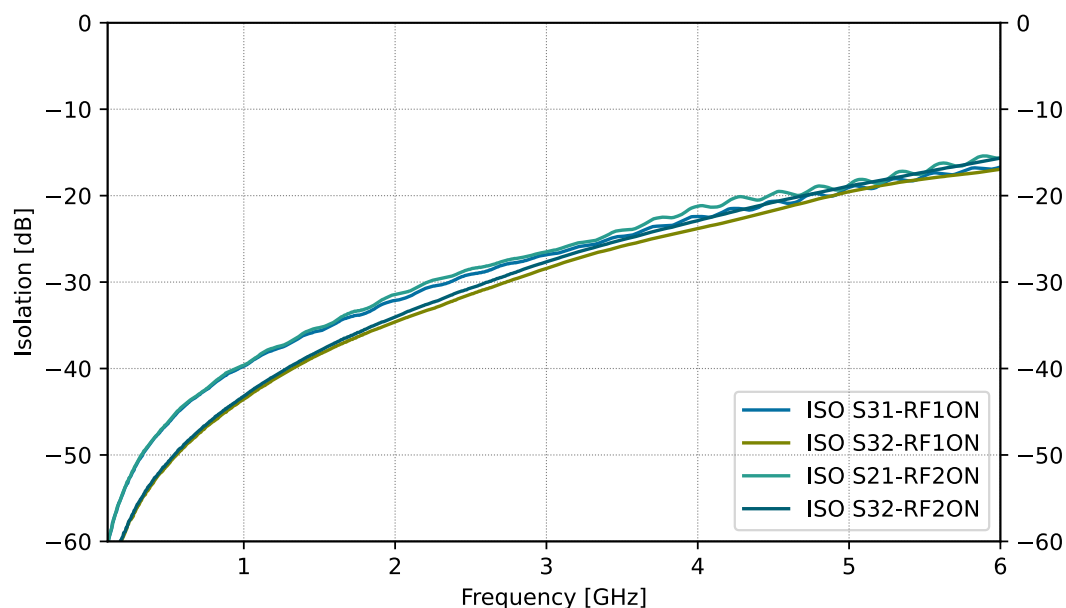
Typical characteristics

Performance upto 2.5 GHz, unmatched

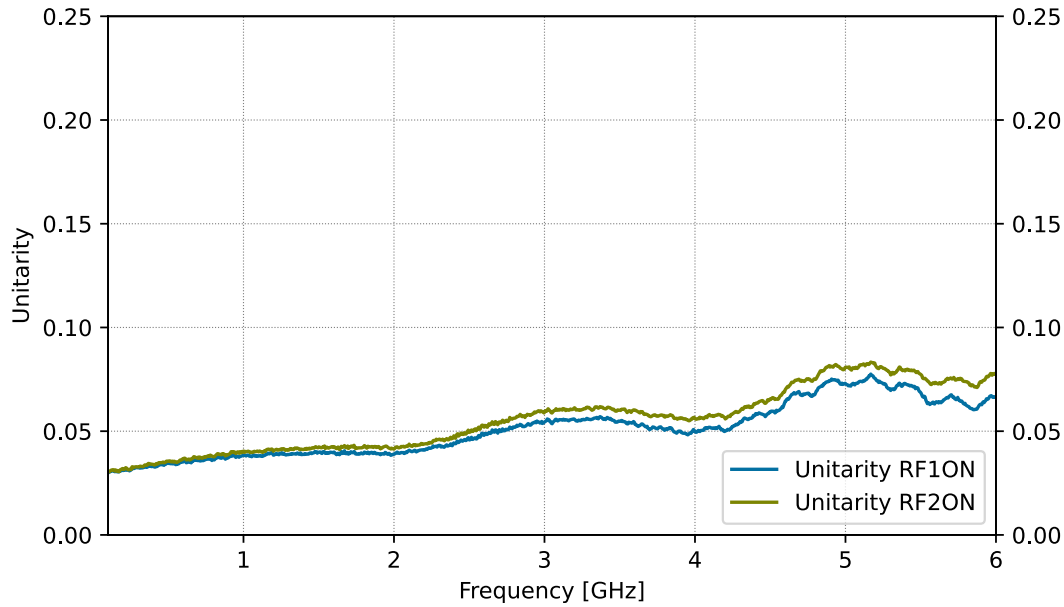
The measurements presented were obtained from the TS8021N EVB with CPW transmission line losses de-embedded from the data. The device operates without requiring matching components for frequencies up to 2.5 GHz. For operation above 2.5 GHz, performance can be enhanced by implementing matching networks at the ports. The provided matching example demonstrates excellent performance up to 5.0 GHz.



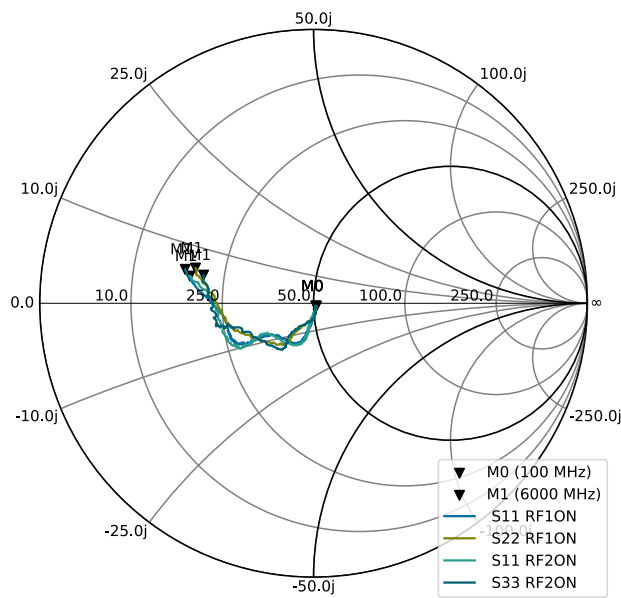
Insertion loss and Return loss, RFC – RFx.



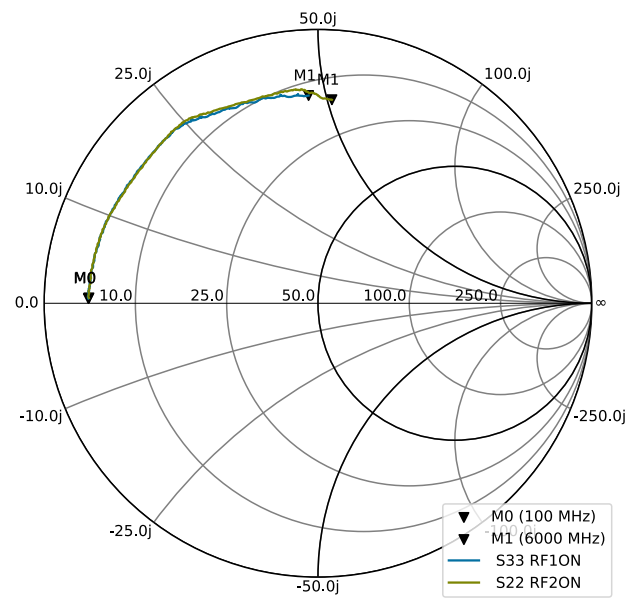
Isolation between RFC to non-active RFx and active RFx to non-active RFx.



Power absorption of the switch $1 - |S_{11}|^2 - |S_{21}|^2 - |S_{31}|^2$, RFC – RFx.



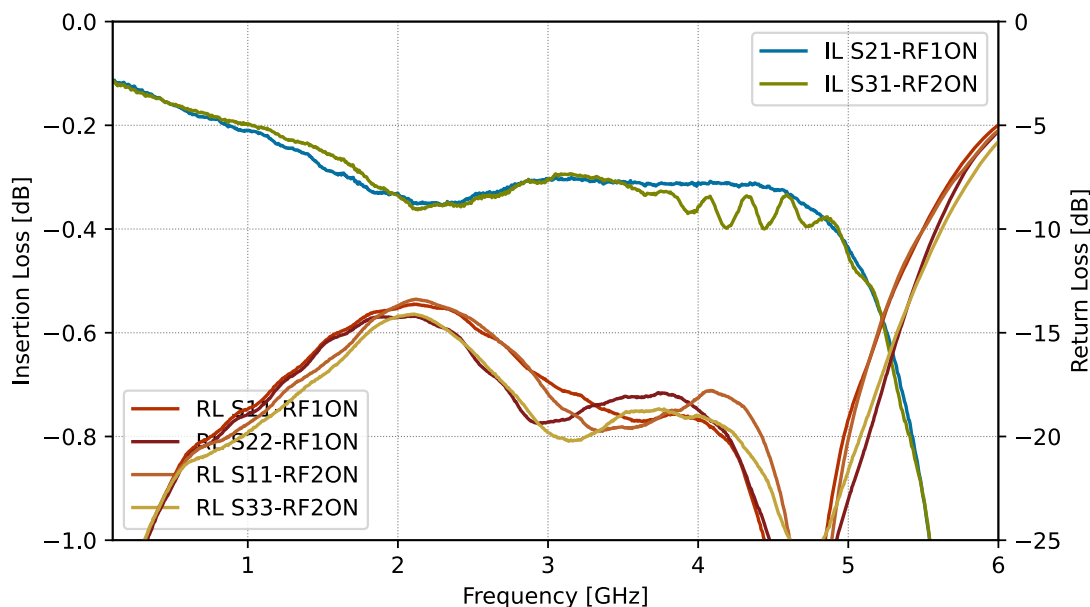
RFC and RFx matching at ON state.



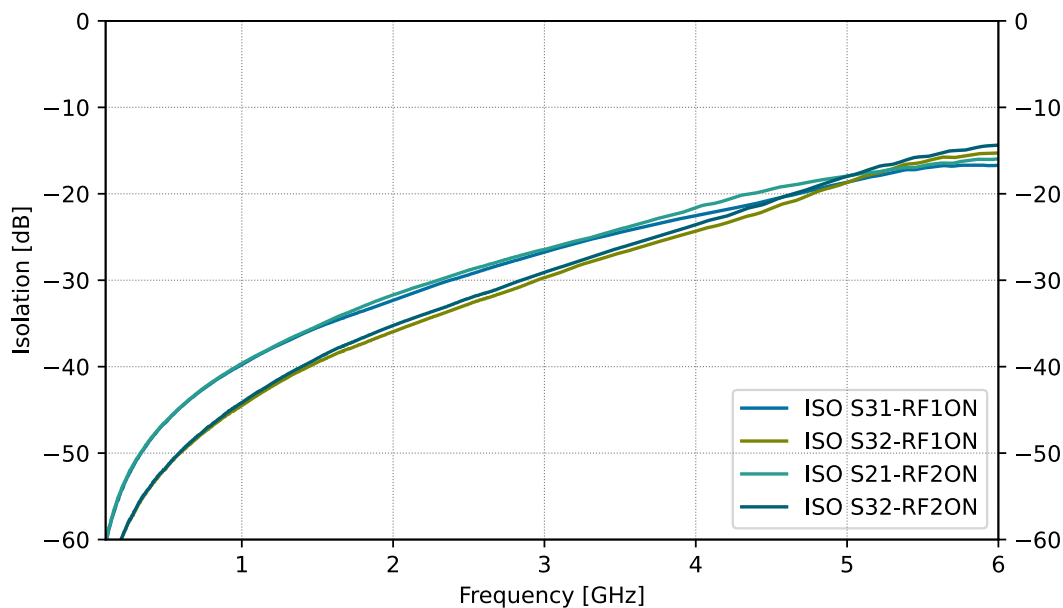
Non-active RFx port impedance. Low frequency starts from closed and circulates towards open at higher frequencies.

Performance upto 5.0 GHz, matching applied to EVB

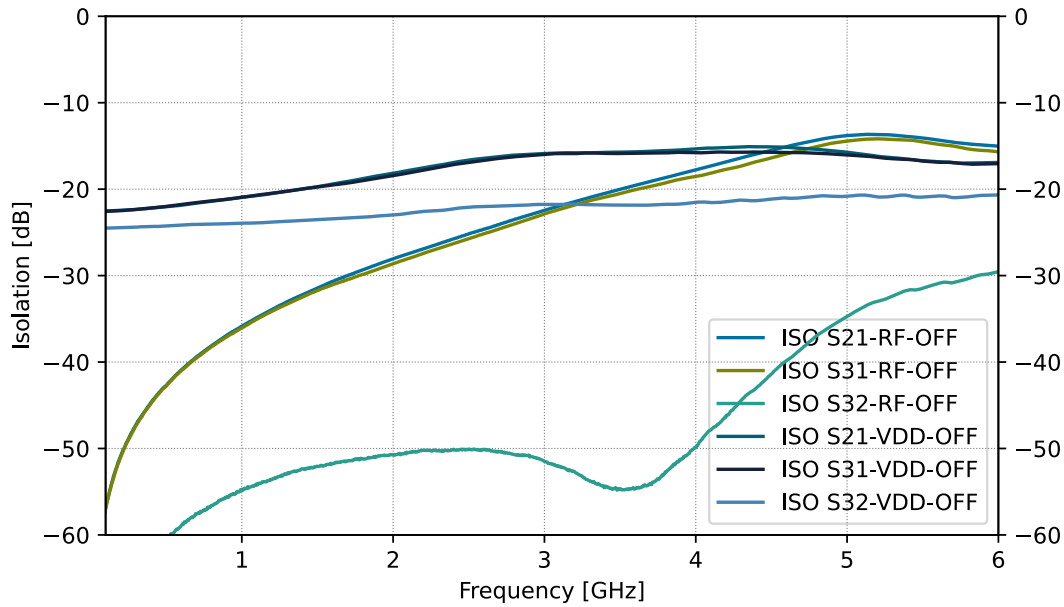
The measurements presented were obtained from the TS8021N EVB with matching components implemented. CPW transmission line losses have been de-embedded from the data; however, matching component losses remain included. This example matching configuration extends the corner frequency to 5.0 GHz. For narrower band applications, such as 2.5–5.0 GHz operation, the matching network should be optimized to position the corner frequency closer to 5.0 GHz, which will simultaneously improve low-frequency return loss performance.



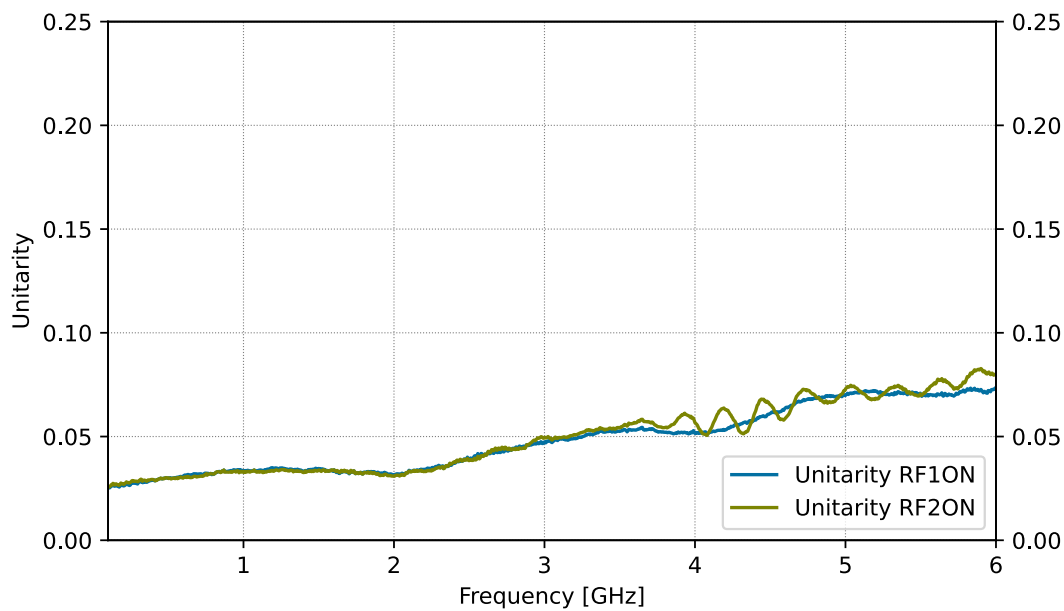
Insertion loss and Return loss, RFC – RFx.



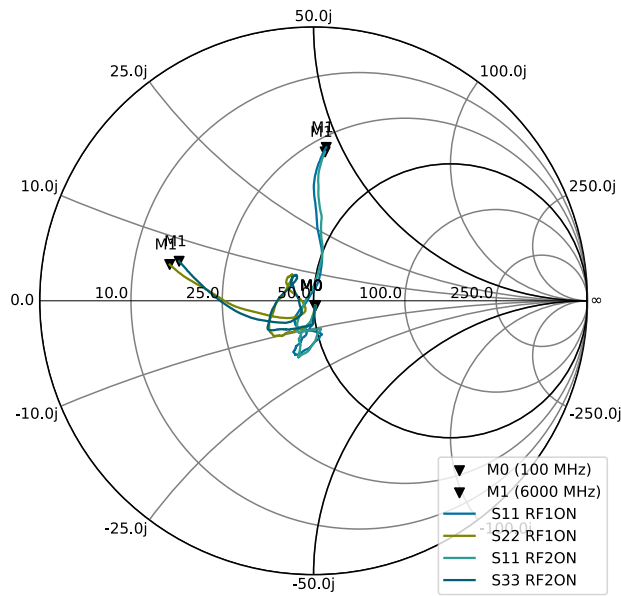
Isolation between RFC to non-active RFx and active RFx to non-active RFx.



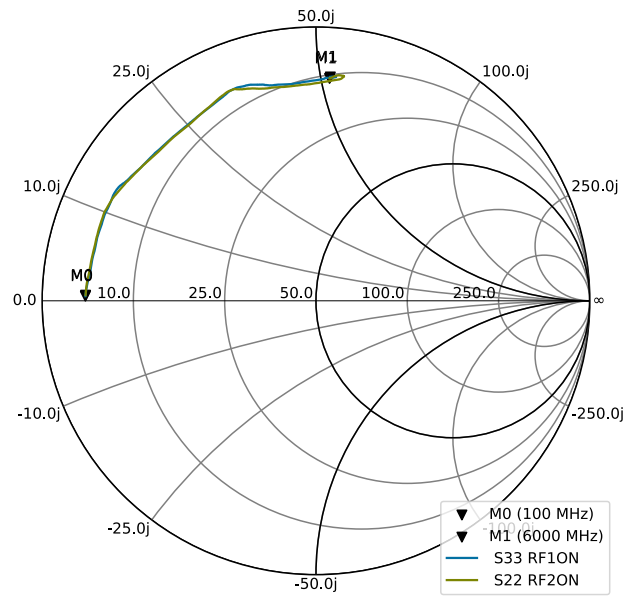
Isolation at V_{DD} off and ISO state between RFC to RFx and RF1 to RF2.



Power absorption of the switch $1 - |S_{11}|^2 - |S_{21}|^2 - |S_{31}|^2$, RFC – RFx.



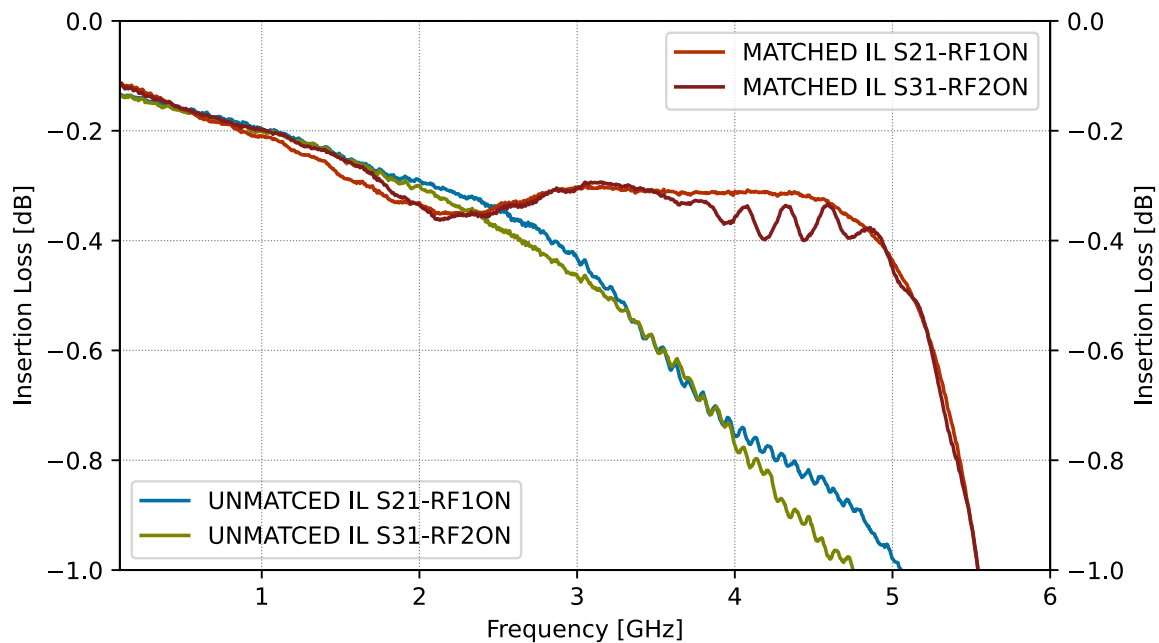
RFC and RFx matching at ON state.



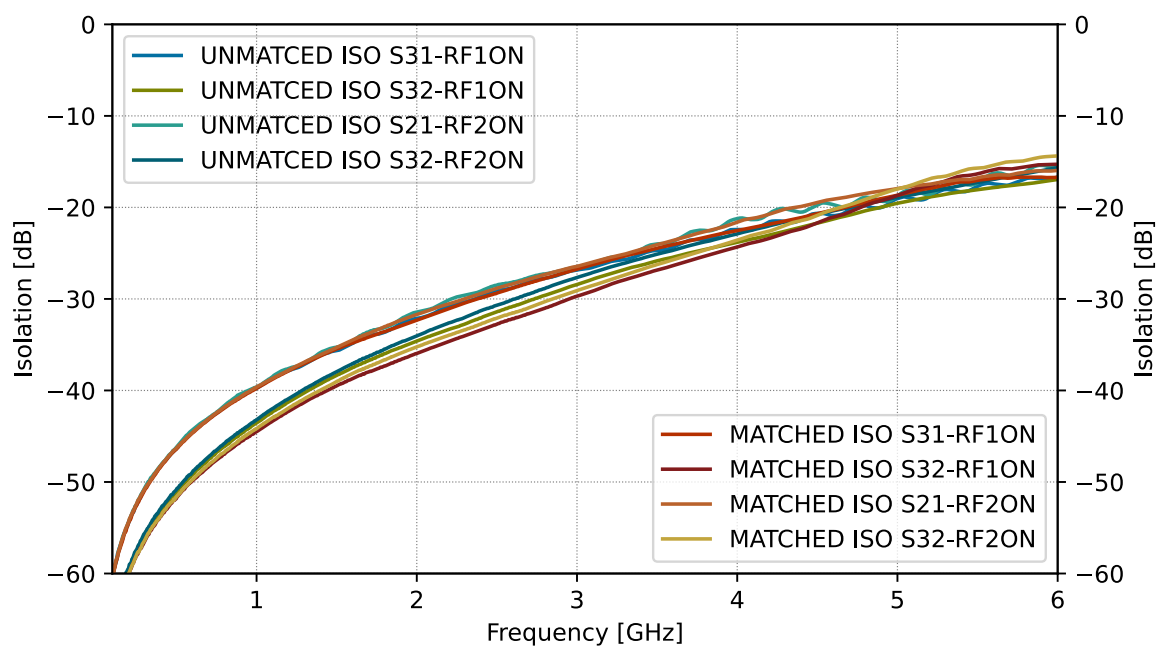
Non-active RFx port impedance. Low frequency starts from closed and circulates towards open at higher frequencies.

Comparison plots of matched and unmatched switch.

The following figures present Insertion Loss and Isolation comparison plots for matched and unmatched switch configurations. The matching network extends the usable frequency response up to 5.0 GHz while maintaining isolation performance. For applications operating above 2.5 GHz, implementing the matching network is recommended to optimize performance in this frequency range.



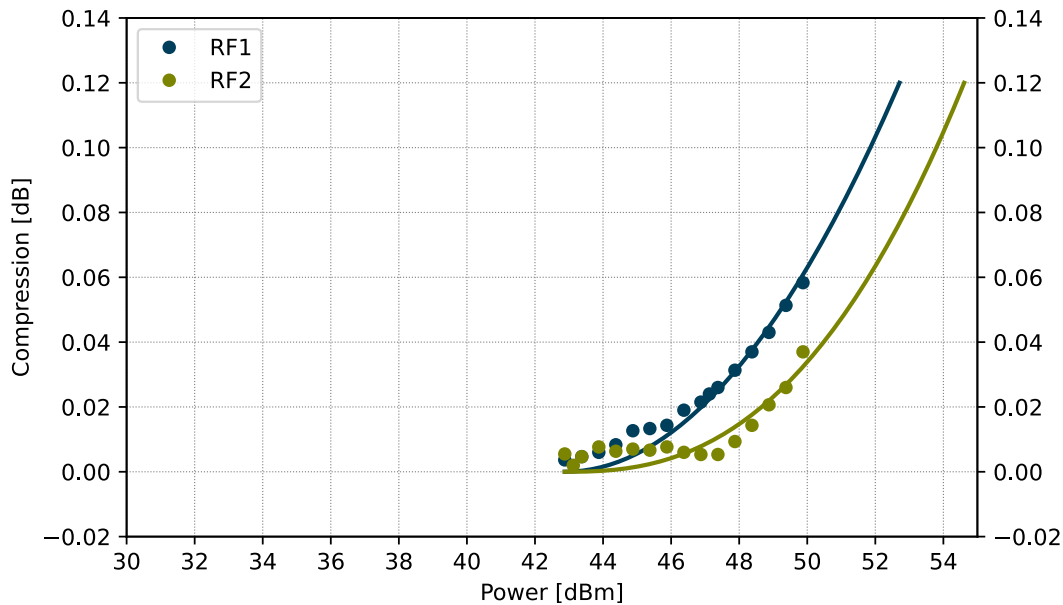
Insertion loss comparison of matched to 5.0 GHz and unmatched switch.



Isolation comparison of matched to 5.0 GHz and unmatched switch.

Compression measurement

In TagoreTech RF switches, compression is primarily attributed to thermal effects resulting from increased ohmic losses within the device. TS8021N switch exhibits no signal clipping at the 0.1 dB compression point. All measurements were conducted at ambient temperature $T_A = +25^\circ\text{C}$. The presented data represents the average of three device samples. Measured data points are indicated by dots (•), while modeled predictions and extrapolations are shown as dashed lines (–).



0.1 dB Compression point measurement, out of 3 parts, results averaged.

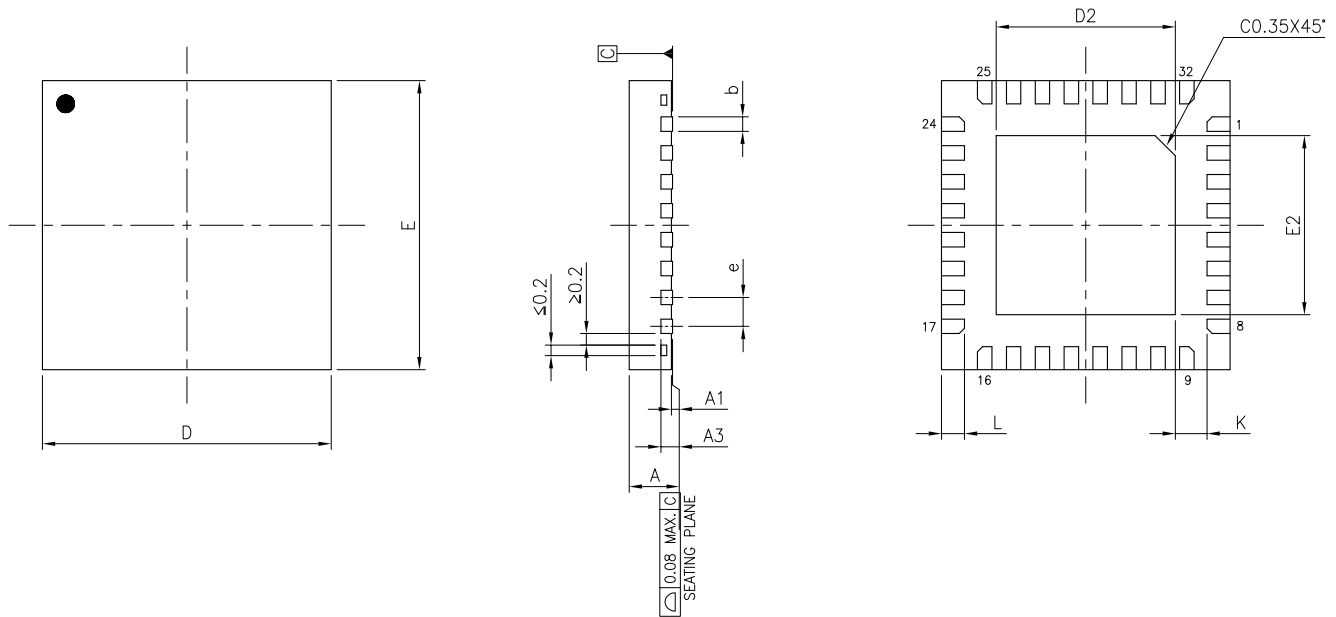
Power De-rating

TS8021N has power handling de-rating below 10MHz. Power de-rating table has been defined for 50 ohm environment. Tagore is currently working with additional measurements and will update power de-rating table in future. This table is conservative, our preliminary indication is that part can handle higher power at lower frequencies. Customers are advised to carry out their own measurements when operating below 30MHz.

Table 13: Power De-Rating table

Start f	Stop f	Max Power	Unit
1 MHz	2 MHz	40	dBm
2 MHz	5 MHz	43	dBm
5 MHz	10 MHz	45	dBm
10 MHz	400 MHz	47	dBm
400 MHz	$f_{\max}$	47	dBm

Device Package information

Figure 8: 32-pin QFN 5.0 x 5.0 x 0.75 mm³ package drawing.

Please refer to application notes TN-001 and TN-002 at TagoreTech web page for PCB and soldering guidelines.

Table 14: Device Package Dimensions

Dimension	Value [mm]	Tolerance [mm]	Dimension	Value [mm]	Tolerance [mm]
A	0.75	±0.05	e	0.50 BSC	±0.05
A1	0.02	+0.03/-0.02	E	5.00 BSC	±0.05
A3	0.203	REF	E2	3.20	±0.05
b	0.25	+0.05/-0.07	L	0.40	±0.05
D	5.00 BSC	±0.05	K	0.50	±0.05
D2	3.20	±0.05			

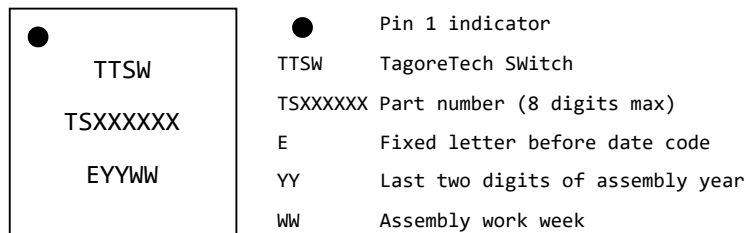


Figure 9: Part marking specification.

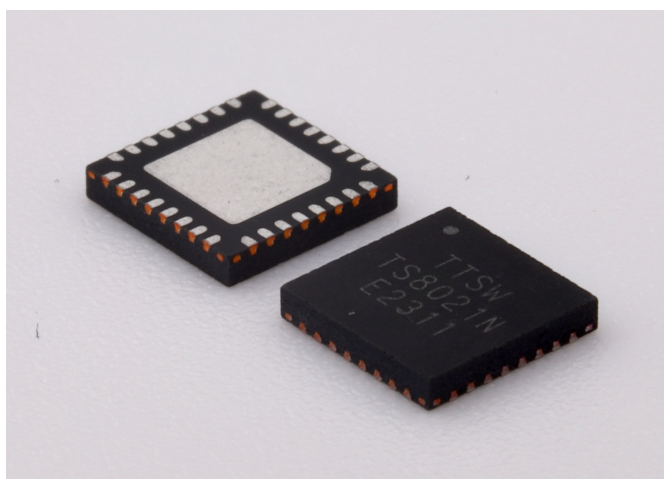


Figure 10: TS8021N photo, top and bottom side.

PCB Land Design

Notes:

- 4-layer PCB is recommended.
- Via diameter is recommended to be 0.3mm to prevent solder wicking inside the vias.
- Thermal vias shall only be placed on the center pad.
- The maximum via number for the center pad is $11(X) \times 11(Y) = 121$.

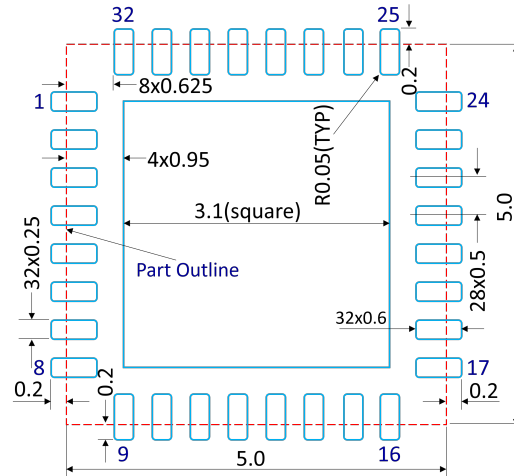


Figure 11: PCB land pattern, dimensions in [mm].

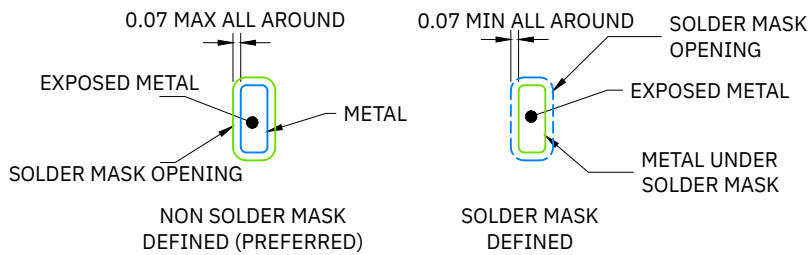


Figure 12: Solder mask opening, dimensions in [mm].

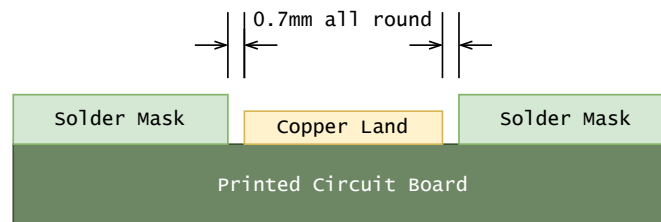


Figure 13: Preferred solder mask opening, side view, dimensions in [mm].

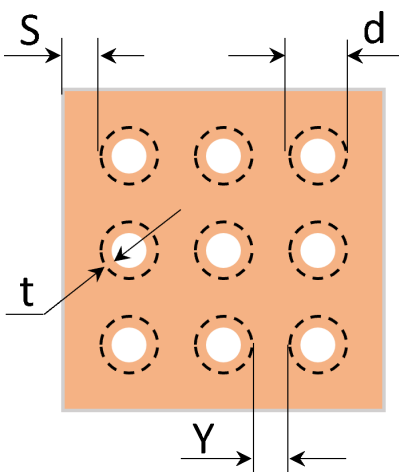


Figure 14: Thermal Via Pattern, Recommended Values: $S \geq 0.15\text{mm}$; $Y \geq 0.20\text{mm}$; $d = 0.2\text{mm}$; Plating Thickness $t = 25\mu\text{m}$ or $50\mu\text{m}$.

PCB Stencil Design

Notes:

- Laser-cut, stainless steel stencil is recommended with electro-polished trapezoidal walls to improve the paste release.
- Stencil thickness is recommended to be 125 μ m.

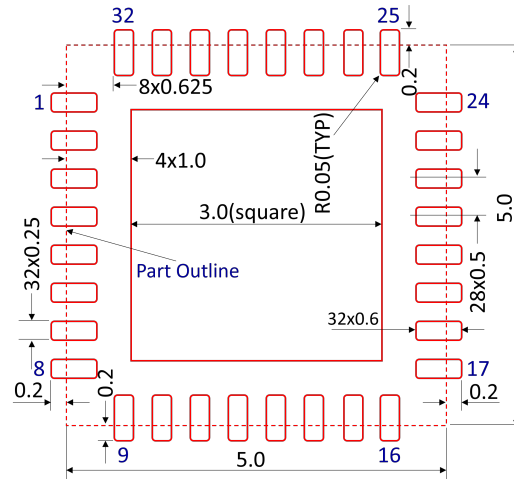


Figure 15: Stencil Openings, dimensions in [mm].

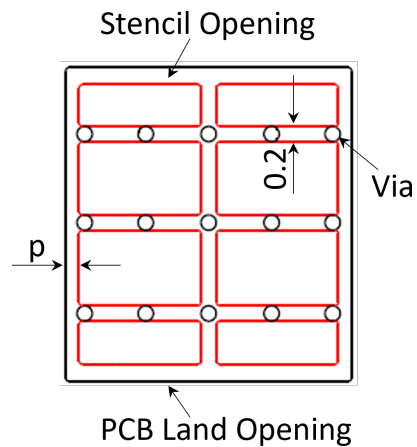
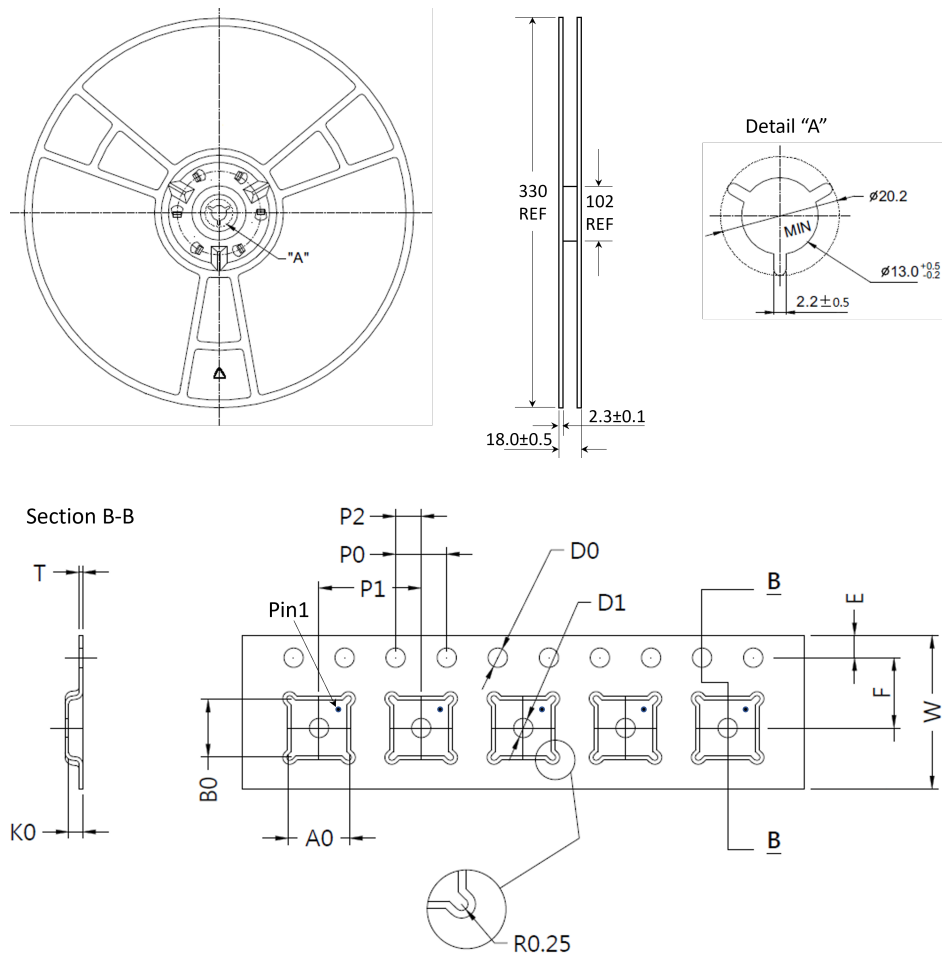


Figure 16: Stencil Openings Shall not Cover Via Areas If Possible, dimensions in [mm].

Tape and Reel Information



A0[mm]	B0[mm]	D0[mm]	D1[mm]	E[mm]	F[mm]	K0[mm]	P0[mm]	P1[mm]	P2[mm]	T[mm]	W[mm]
5.25	5.25	1.50	1.50	1.75	5.50	1.10	4.00	8.00	2.00	0.30	12.00

Glossary

IL	Insertion loss
ISO	Isolation
RL	Return loss
VSWR	Voltage Standing Wave Ratio
RFC	RF Common port, sometimes referred as ANT
RFx	RF Port number x
Unitarity	Describes power absorption of the component, $1 - S_{11} ^2 - S_{21} ^2 - S_{31} ^2$

Changelog

Table 15: Changelog

Date	Revision	Notes
02/01/2026	3.0	New release with updated information, frequency range extended from 1 MHz to 5.0 GHz
04/16/2026	3.01	RoHS10 and Control voltage maximum equal to Vdd
05/19/2026	3.02	Compression plot added and switching time characterization frequency.
09/07/2026	3.03	Package dimensions corrected, Table of Contents added, typos corrected
09/19/2026	3.04	Package thickness corrected from 0.85mm to 0.75mm, Legal Information page updated

Legal Information

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