

GRF4005

BROADBAND LNA / LINEAR DRIVER 0.1 to 3.8 GHz

FEATURES

- Flexible Bias Voltage and Current
- Internally Matched to 50 Ω
- Process: GaAs pHEMT
- Compact 1.5 x 1.5 mm DFN-6 Package

Reference: 5 V / 170 mA / 2.5 GHz

- Gain: 12.8 dB
- OIP3: 42 dBm
- OP1dB: 27.5 dBm
- Evaluation Board Noise Figure: 0.88 dB

APPLICATIONS

- Linear Driver Amplifier
- Small Cells and Cellular Repeaters
- Distributed Antenna Systems
- First/Second Stage LNA

DESCRIPTION

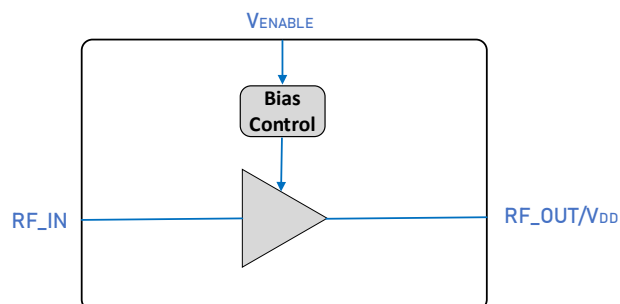
The GRF4005 is a broadband low noise gain block designed for small cell, wireless infrastructure and other high-performance applications. With simple external matching, it exhibits outstanding broadband noise figure (NF), linearity and return losses over wide fractional bandwidths with a single match.

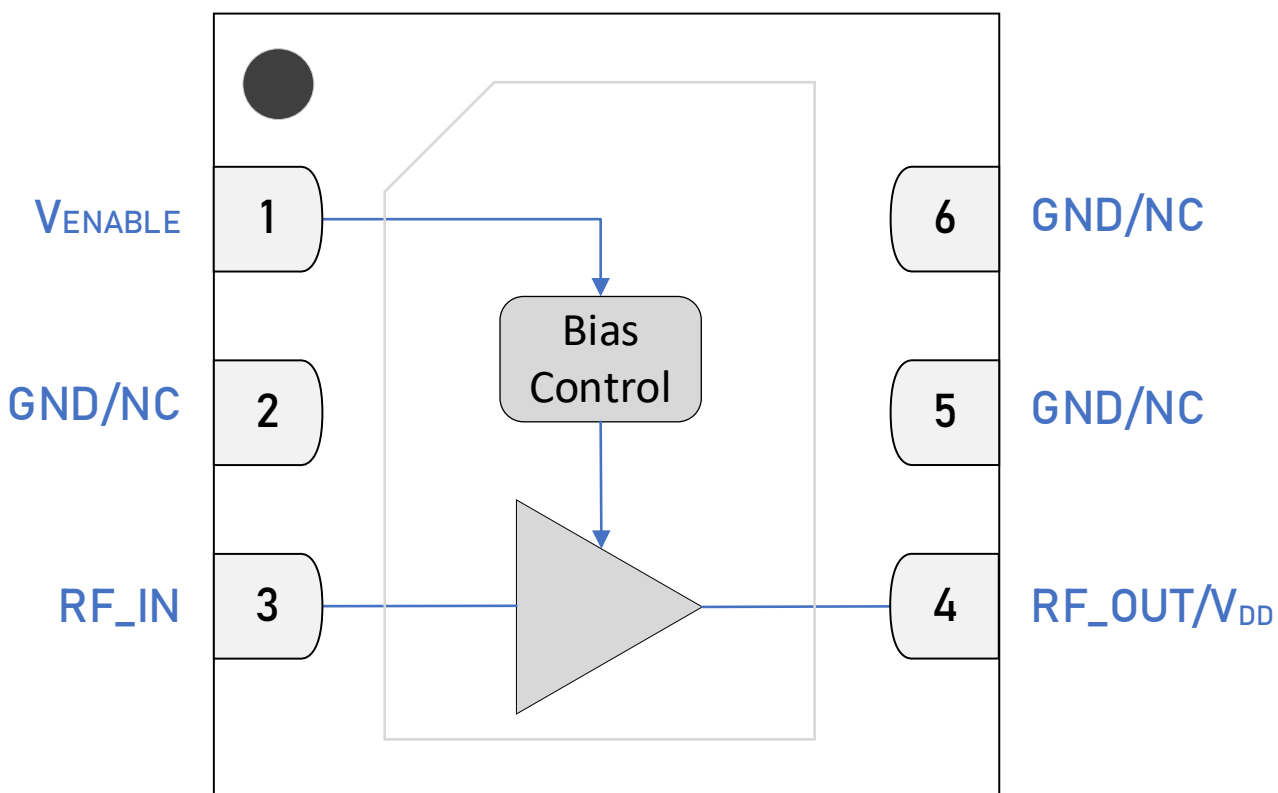
Configured as a first stage LNA, linear driver or cascaded gain block, GRF4005 offers high levels of reuse both within a design and across platforms. The device is operated from a supply voltage (V_{DD}) of 1.8 to 5.5 volts. I_{DDQ} can be adjusted over a wide range for optimal efficiency and linearity.

Please consult with the GRF applications engineering team for custom tuning/evaluation board data and S-parameters.

Additional tunes can be found on the GRF4005 "Custom Tunes" product page: [GRF4005 Custom Tunes](#)

BLOCK DIAGRAM





1.5 x 1.5 mm DFN-6 Pin Out (Top View)

Pin Assignments

Pin	Name	Description	Note
1	V _{ENABLE}	Enable Voltage Input	V _{ENABLE} and series resistor set I _{DDQ} . V _{ENABLE} ≤ 0.2 volts disables device. On die pull-down resistor will turn the device off if this node is allowed to float.
2, 5, 6	GND/NC	Ground or No Connect	No internal connection to die. We recommend connecting these pins to ground.
3	RF_IN	LNA RF Input	An external DC blocking capacitor must be used.
4	RF_OUT/V _{DD}	LNA RF Output	V _{DD} must be applied through a choke to this pin.
PKG BASE	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.

Absolute Ratings

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	V_{DD}	0	6	V
RF Input Power: Load VSWR < 2:1, $V_{DD} = 5$ V.	$P_{IN\ MAX}$		22	dBm
Operating Temperature (package base).	$T_{PKG\ BASE}$	-40	105	°C
Maximum Channel Temperature (MTTF > 10 ⁶ hours).	T_{MAX}		170	°C
Maximum Dissipated Power	$P_{DISS\ MAX}$		850	mW

Electrostatic Discharge

Human Body Model	HBM	500		V
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Storage

Storage Temperature	T_{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		1	--



Caution! ESD Sensitive Device.

Exceeding Absolute Maximum Rating conditions may cause permanent damage.

Note: For additional information, please refer to [Package Manufacturing Information | Guerrilla RF \(guerrilla-rf.com\)](#)



All Guerrilla RF products are provided in RoHS compliant lead (Pb)-free packaging requiring no exemptions. Additional information for this topic can be found at this link - [Environmental and Restricted Substance Statement Library](#)

Recommended Operating Conditions

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Supply Voltage	V_{DD}	1.8	5	6	V	
Operating Temperature (package base)	$T_{PKG\ BASE}$	-40		105	°C	
RF Frequency Range	F_{RF}	0.1	2.5	3.8	GHz	Typical application schematic with external matching components (notes 1 & 2).
RF_IN Port Impedance	Z_{RF_IN}		50		Ω	
RF_OUT Port Impedance	Z_{RF_OUT}		50		Ω	

Note 1: Operation outside of this range is supported by using different custom tunes. Examples of other optimized tunes can be found here: [GRF4005 Custom Tunes](#).

Note 2: Contact the Guerrilla RF applications team for guidance on optimizing the tuning of the device for alternative bands.

Nominal Operating Parameters – General

The following conditions apply unless noted otherwise: typical application schematic using the 0.1 to 3.8 GHz tuning set. $V_{DD} = 5\text{ V}$, $F_{TEST} = 2.5\text{ GHz}$, $50\ \Omega$ system impedance, $T_{PKG\ BASE} = 25\text{ }^{\circ}\text{C}$. Evaluation board losses are included within the specifications.

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Supply Current	I_{DD}	145	170	195	mA	$V_{DD} = 5\text{ V}$, $V_{ENABLE} = 5\text{ V}$.
Switching Rise Time	T_{RISE}		500		ns	Gain mode to Disabled mode (note 3).
Switching Fall Time	T_{FALL}		500		ns	Disabled mode to Gain mode (note 4).

Disabled Mode

Leakage Current	$I_{LEAKAGE}$		4.5	20	μA	$V_{DD} = 5\text{ V}$, $V_{ENABLE} = 0\text{ V}$.
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Thermal Data

Thermal Resistance (Infrared Scan)	Θ_{JC}		87		$^{\circ}\text{C}/\text{W}$	On standard evaluation board (note 5).
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Note 3: Switching Time: 50% of V_{ENABLE} to 90% of P_{OUT} .

Note 4: Switching Time: 50% of V_{ENABLE} to 10% of P_{OUT} .

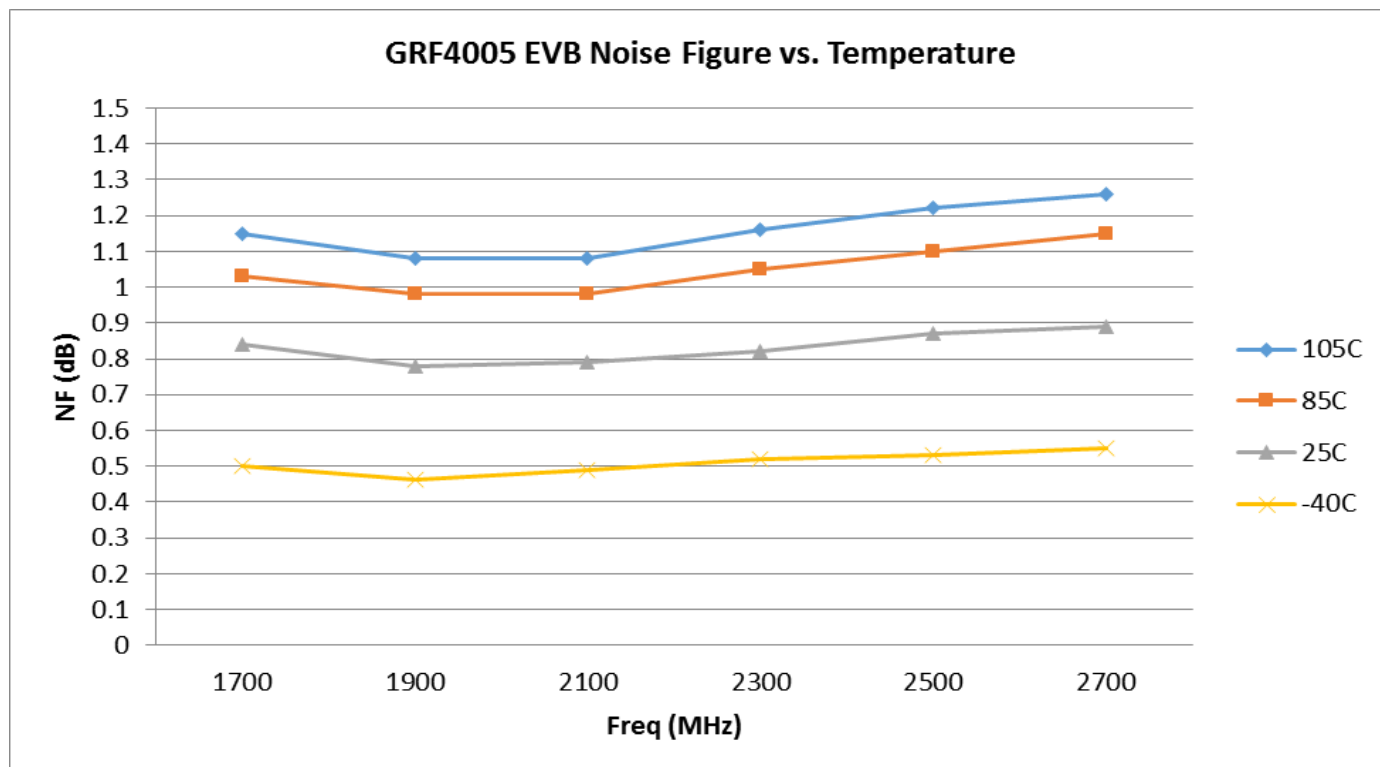
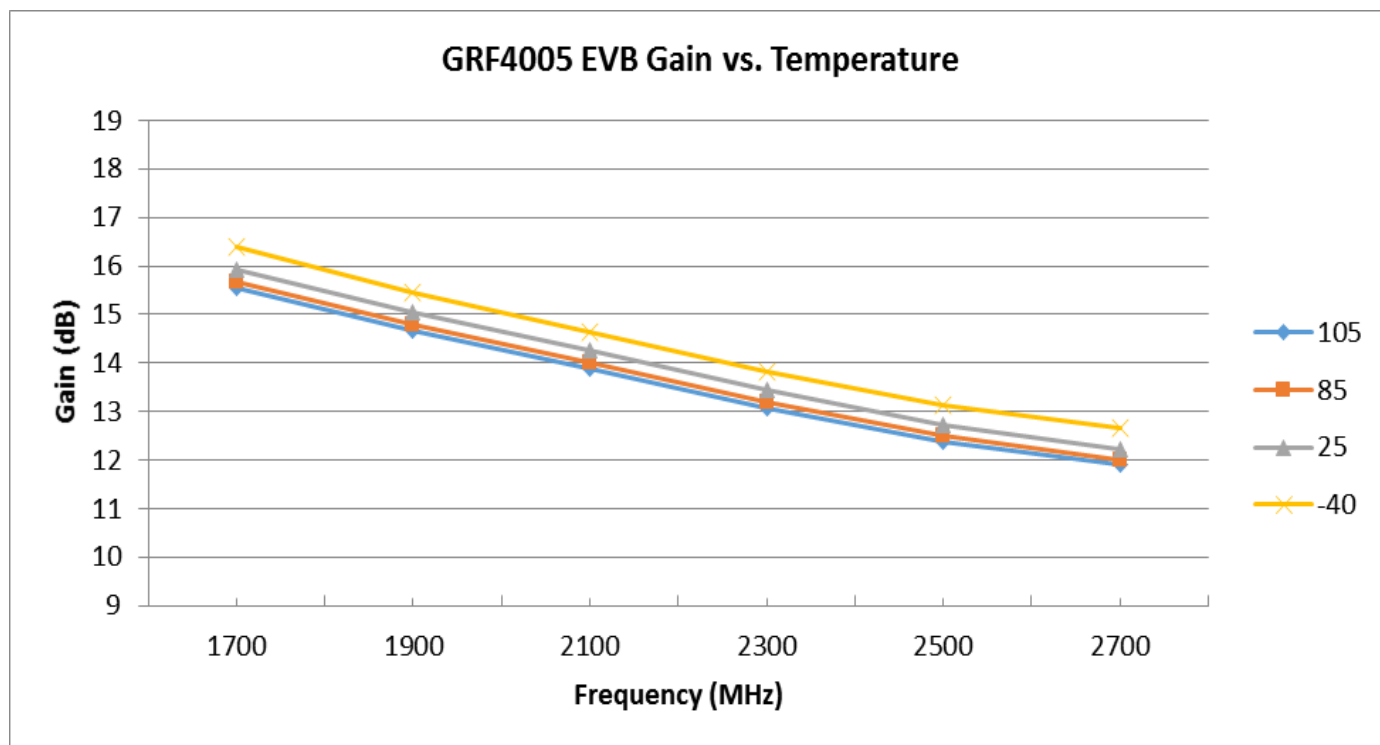
Note 5: MTTF > 10^6 hours for $T_{CHANNEL} \leq 170\text{ }^{\circ}\text{C}$.

Nominal Operating Parameters – RF

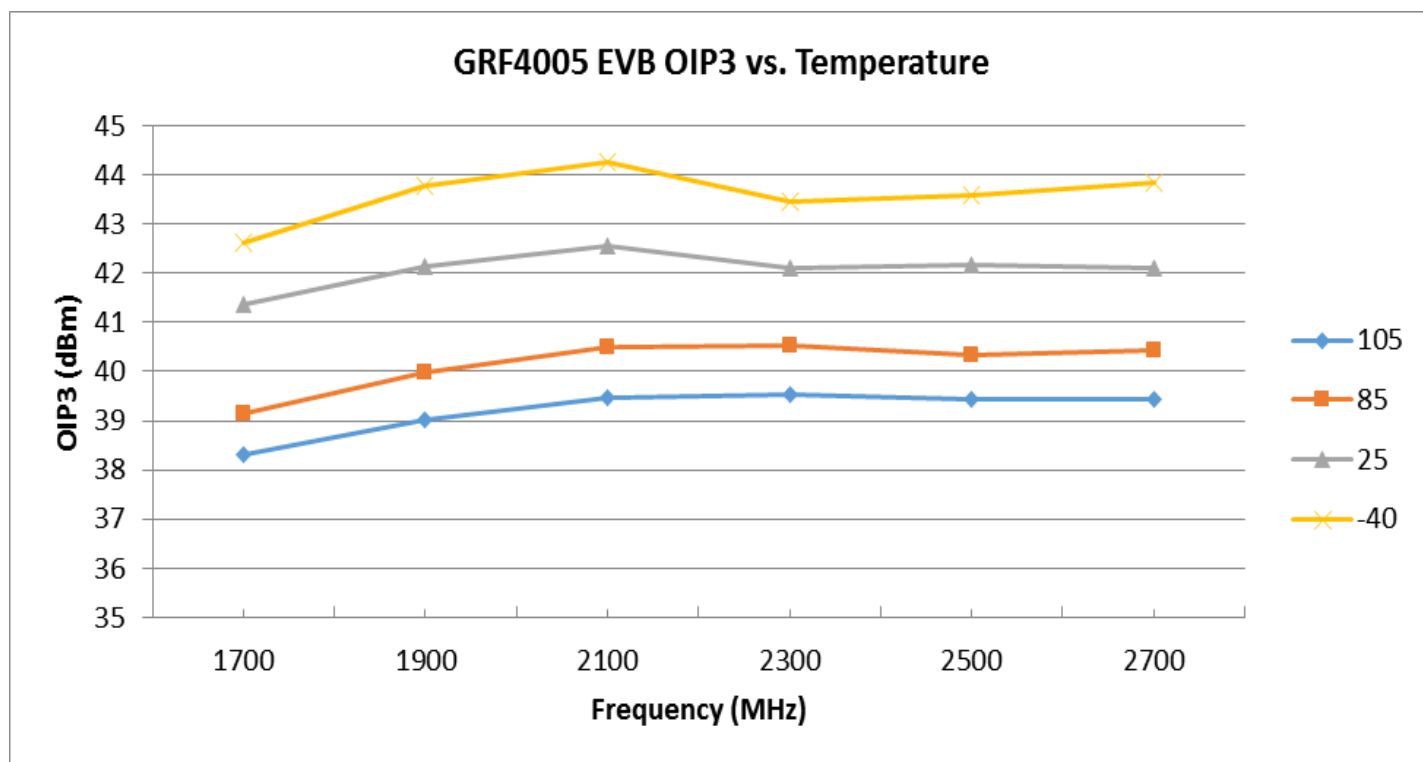
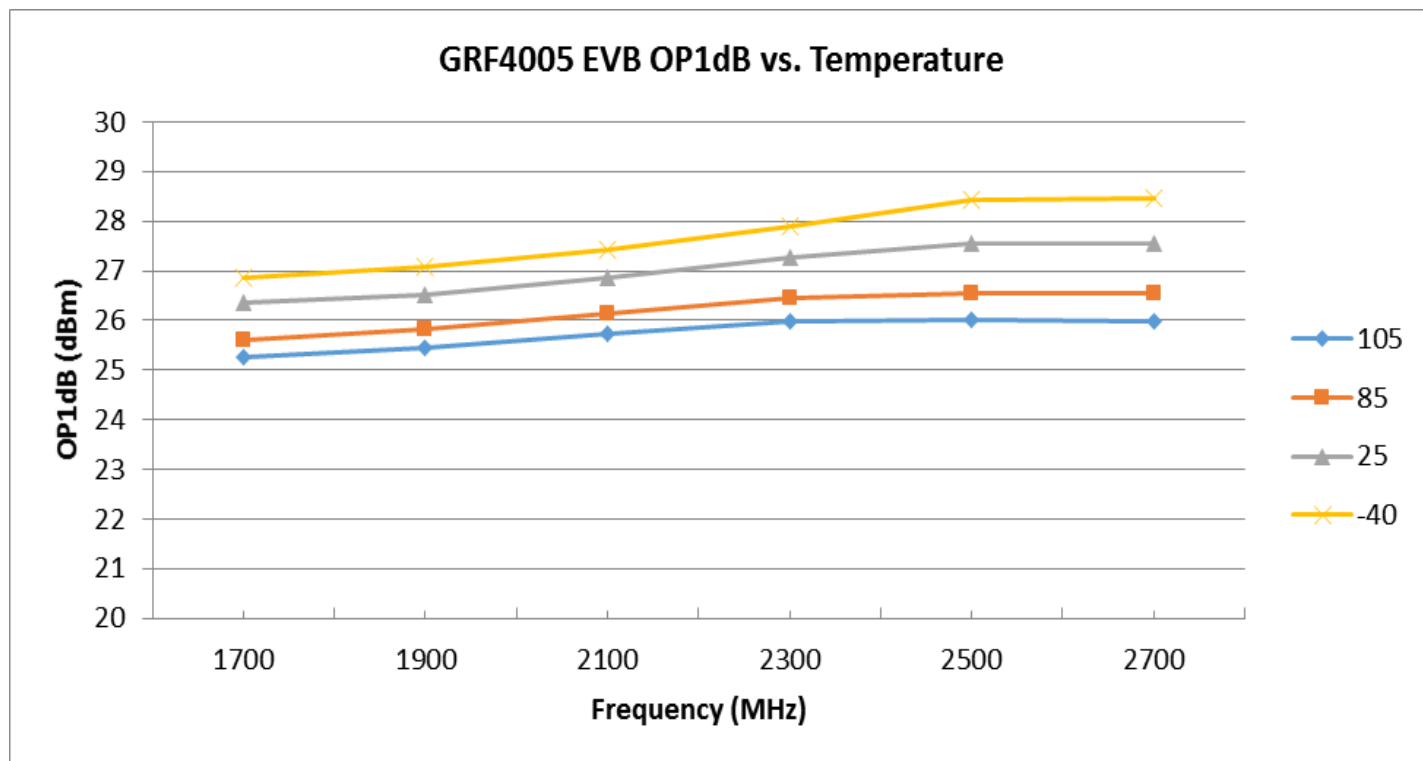
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Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Gain	S21	11.8	12.8		dB	
Noise Figure	NF		0.88	1.05	dB	On standard evaluation board.
Output 3 rd Order Intercept Point	OIP3		42		dBm	2 dBm P_{OUT} per tone at 2 MHz spacing (2499 and 2501 MHz).
Output 1 dB Compression Power	OP1dB	26	27.5		dBm	

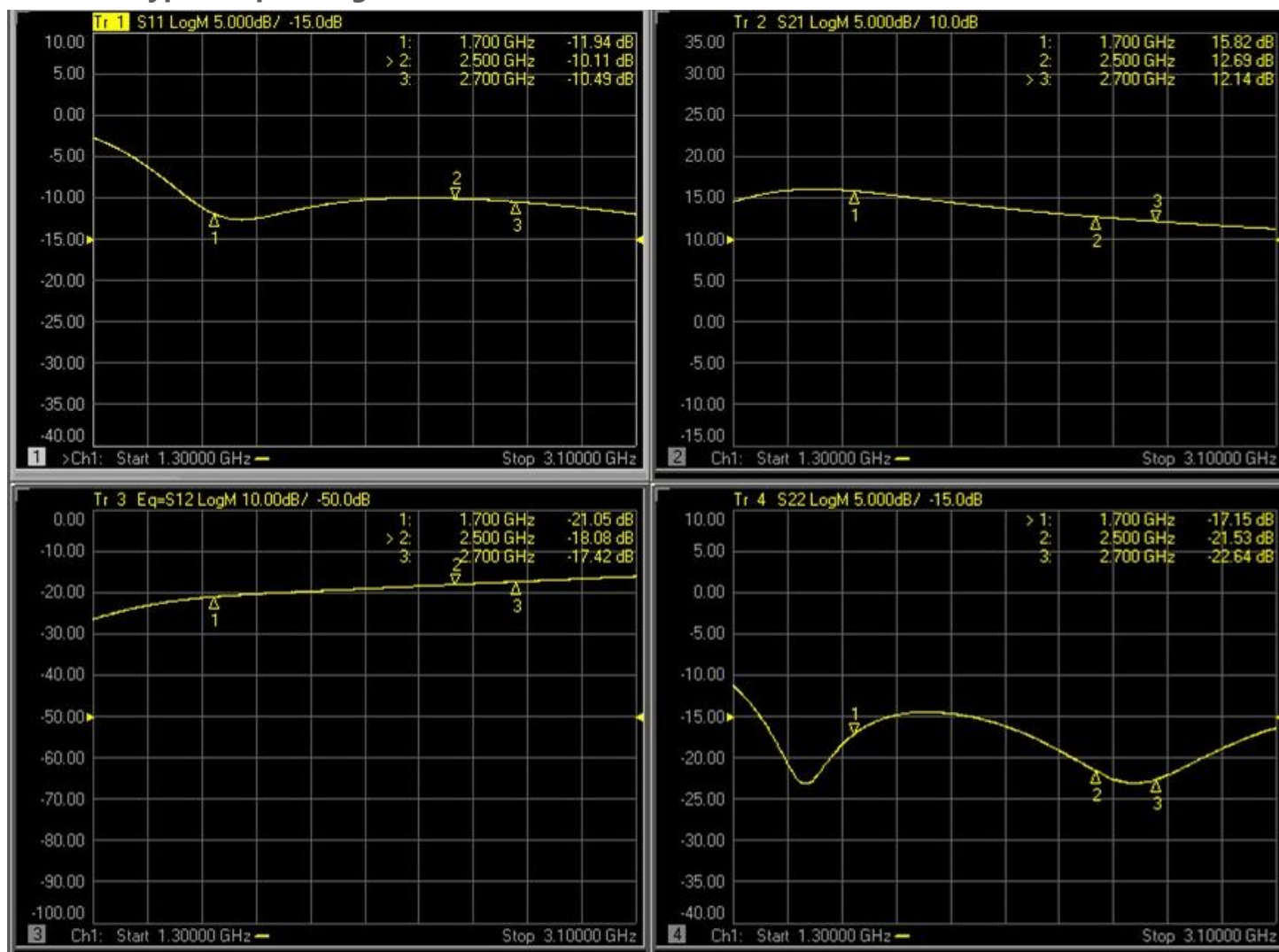
GRF4005 Typical Operating Curves: 1.7 to 2.7 GHz Tune



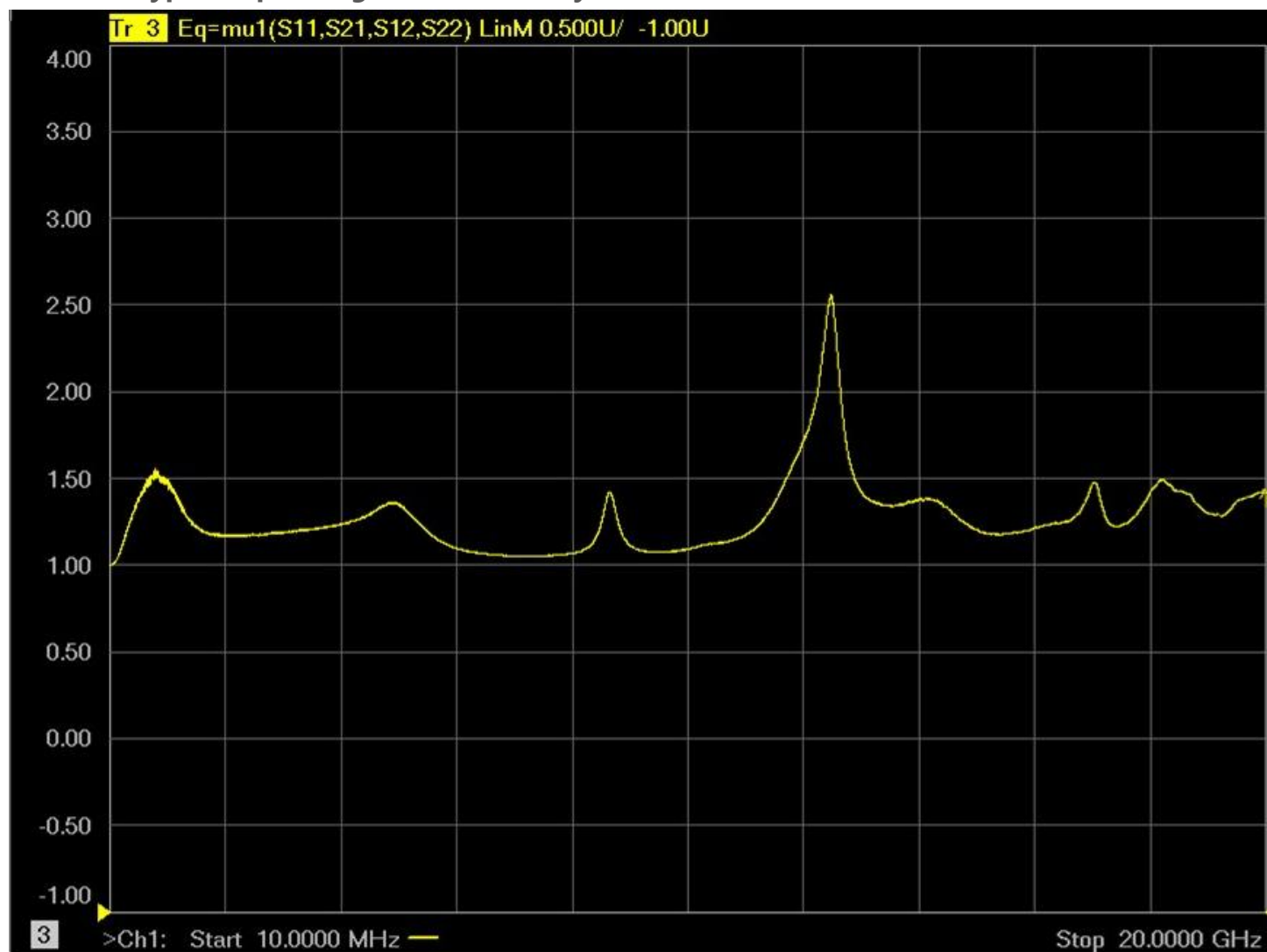
GRF4005 Typical Operating Curves: 1.7 to 2.7 GHz Tune



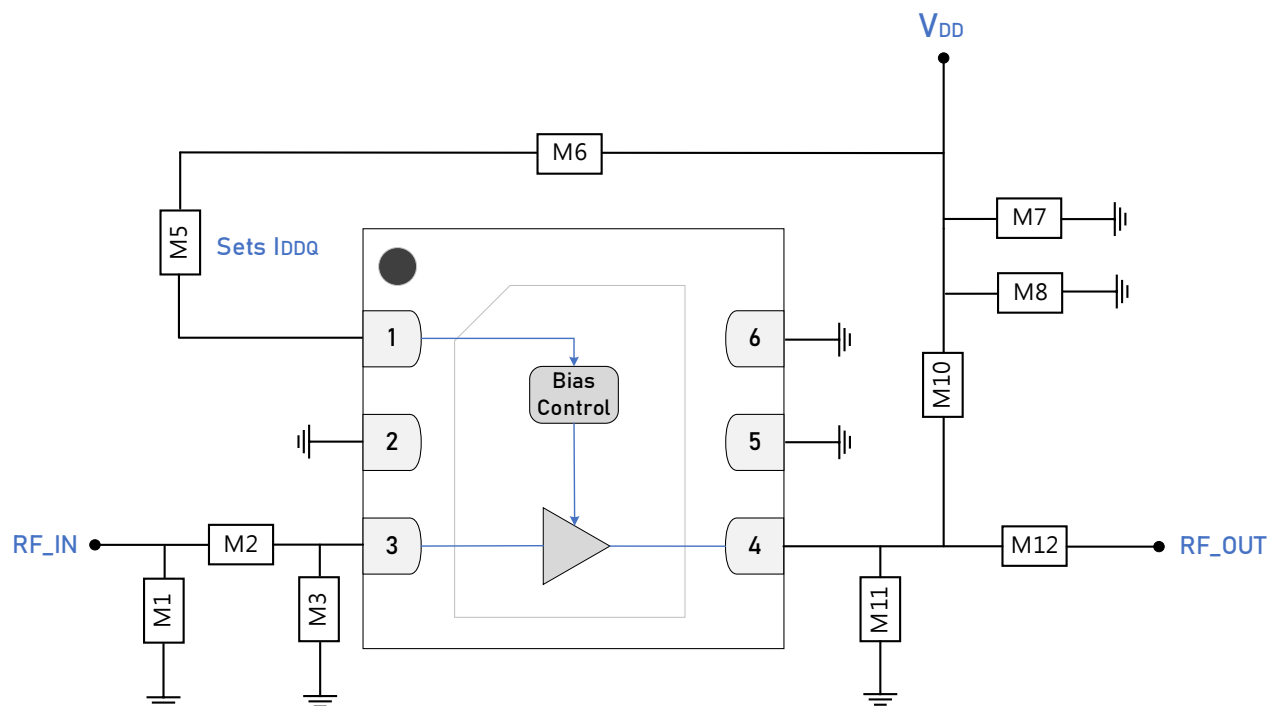
GRF4005 Typical Operating Curves: S-Parameters (1.7 to 2.7 GHz Tune)



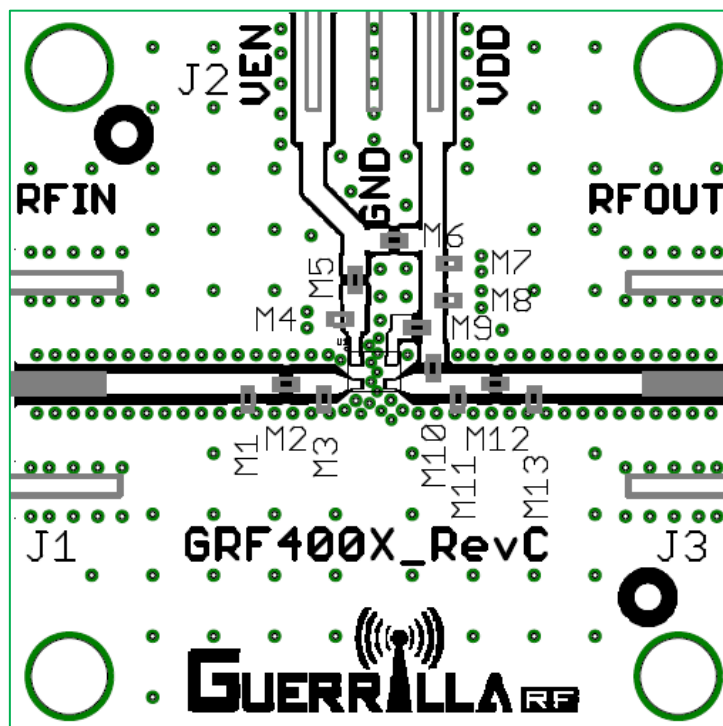
GRF4005 Typical Operating Curves: Stability Mu Factor (10 MHz to 20 GHz)



Note: $\mu \geq 1.0$ implies unconditional stability.



GRF4005 Standard Evaluation Board Schematic

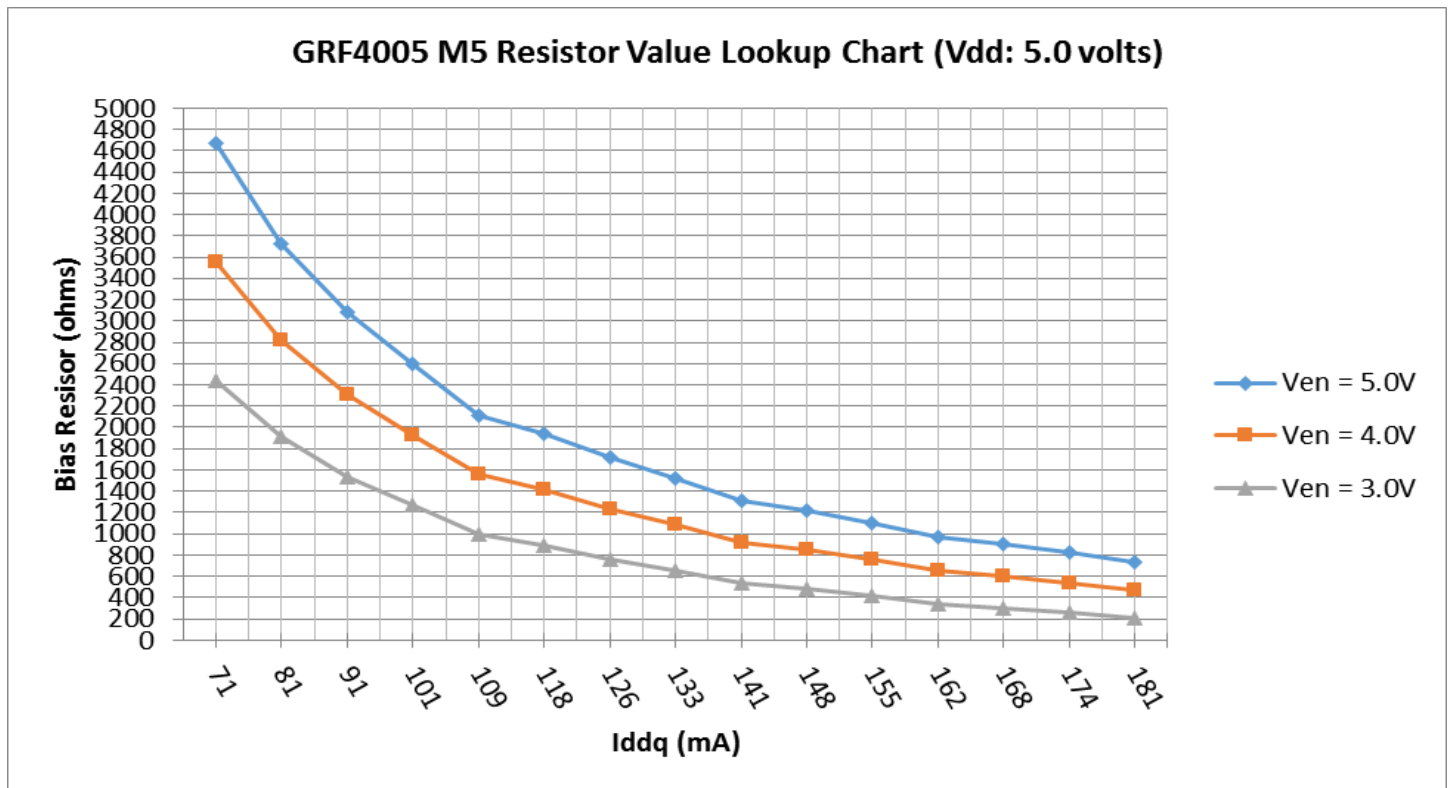


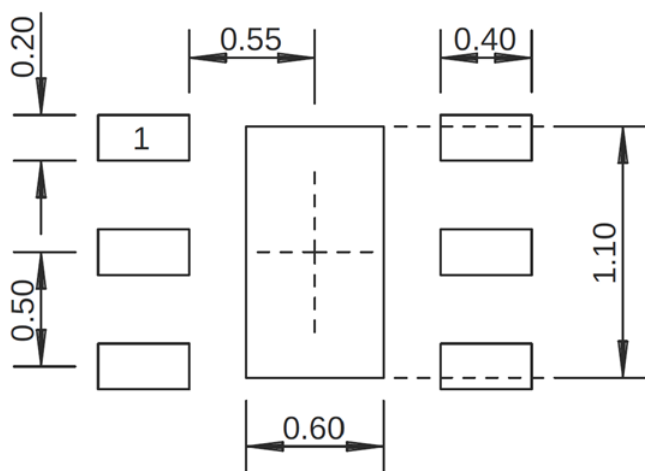
GRF4005 Evaluation Board Assembly Diagram

GRF4005 Evaluation Board Assembly Diagram Reference: 1.7 to 2.7 GHz Tune

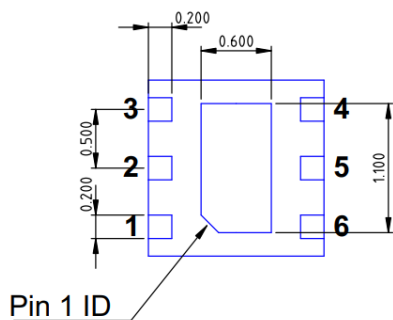
Component	Type	Manufacturer	Family	Value	Package Size	Substitution
M1	Inductor	Coilcraft	HPA	3.3 nH	0402	ok
M2	Capacitor	Murata	GJM	2.0 pF	0402	ok
M3	Capacitor	Murata	GJM	1.5 pF	0402	ok
M5 (sets I_{DDQ})	Resistor	Various	5%	see curves	0402	ok
M6	Resistor (jumper)	Various	5%	0 Ω	0402	ok
M7	Capacitor	Murata	GRM	0.1 μ F	0402	ok
M8	Capacitor	Murata	GRM	27.0 pF	0402	ok
M10	Inductor	Coilcraft	HPA	18.0 nH	0402	ok
M11	Capacitor	Murata	GJM	1.0 pF	0402	ok
M12	Capacitor	Murata	GJM	15.0 pF	0402	ok
Evaluation Board	GRF400X_RevC					

GRF4005 Bias Resistor Selection Curves

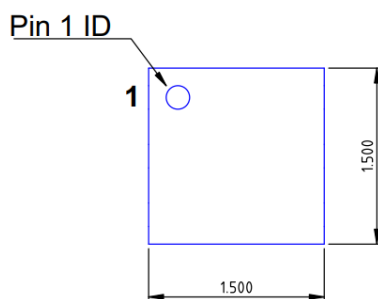




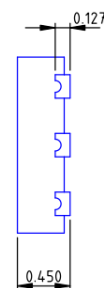
1.5 x 1.5 mm DFN-6 Suggested PCB Footprint (Top View)



Bottom View



Top View



Side View

DFN6 1.5x1.5mm

Dimensions in millimeters
Dimensional Tolerance: ± 0.05

1.5 x 1.5 mm DFN-6 Package Dimensions

Package Marking Diagram



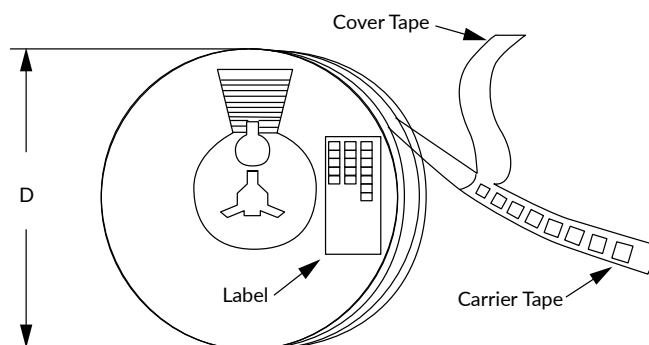
- Line 1: "Y" = YEAR (single digit). "WW" = WORK WEEK the device was assembled.
- Line 2: "XXXX" = Device PART NUMBER.

Tape and Reel Information

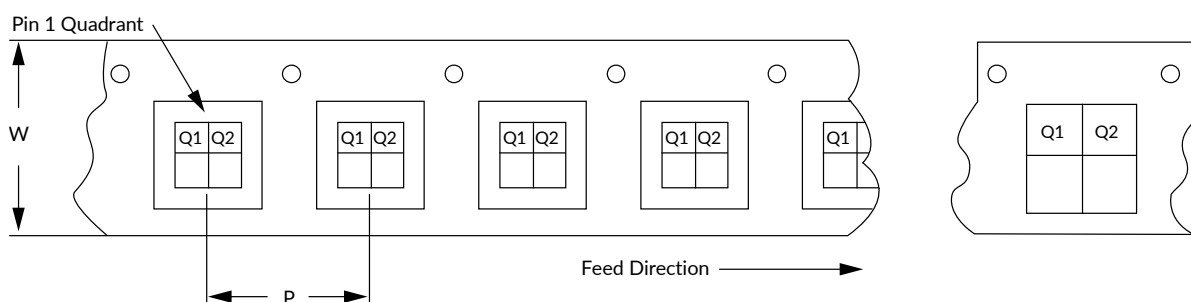
Guerrilla RF's tape and reel specification complies with Electronic Industries Alliance (EIA) standards for "Embossed Carrier Tape of Surface Mount Components for Automatic Handling" (reference EIA-481). See the following page for the Tape and Reel Specification and Device Package Information table, which includes units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape and reeled onto a plastic reel. Each reel is packaged in a cardboard box. There are product labels on the reel, the protective ESD bag and the outside surface of the box.

For the Tape and Reel Reference Table, please refer to: [Package Manufacturing Information | Guerrilla RF \(guerrilla-rf.com\)](#)



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information

Revision History

Revision Date	Description of Change
May 9, 2017	Release A Data Sheet.
February 20, 2025	Upgraded Data Sheet to new format only. No change to device or device specifications.
January 8, 2026	Changed evaluation board to GRF400X_RevC.



Data Sheet Classifications

Data Sheet Status	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry-supplied transistor S-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on limited evaluation board measurements taken within the Guerrilla RF Applications Lab. All parametric values are subject to change pending the collection of additional data.
Release Ø	All data based on measurements taken with <i>production-released</i> material. TYP values are based on a combination of ATE and bench-level measurements, with MIN/MAX limits defined using <i>modelled estimates</i> that account for part-to-part variations and expected process spreads. Although unlikely, future refinements to the TYP/MIN/MAX values may be in order as multiple lots are processed through the factory.
Release A-Z	All data based on measurements taken with production-released material <i>derived from multiple lots which have been fabricated over an extended period of time</i> . MIN/MAX limits may be refined over previous releases as more statistically significant data is collected to account for process spreads.

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